



ANNAMACHARYA INSTITUTE OF TECHNOLOGY & SCIENCES

(AUTONOMOUS)

NEW BOYANAPALLI, RAJAMPET-516126

VLSISD

M. Tech. II Semester (R17)

(Read the instructions given below carefully before filling the application)

Application for (Regular / Supplementary) _____

Month & Year of Examination _____

HALL TICKET NUMBER

--	--	--	--	--	--	--	--	--	--

Name of the Candidate (as per S. S. C.) _____

Father's Name (as per S. S. C.) _____

Mother's Name _____

Date of Birth (as per S. S. C.)

D	D	M	M	Y	Y	Y	Y

Mobile NUMBER

--	--	--	--	--	--	--	--	--	--

E-Mail ID: _____

Details of subjects Registered: (Tick [\checkmark] the appropriate box)

1	7PB321	Testing and Testability	
2	7PC321	Low Power VLSI Design	
3	7PB323	Hardware Software Co-Design	
4	7PC322	ASIC Design	
5	7PC323	Algorithms for VLSI Design Automation	
6	7PC324	Nano Electronics	
7	7PC325	VLSI Signal Processing	
8	7PB324	DSP Processors and Architectures	
9	7PC326	Seminar – II	
10	7PC327	VLSI Design Laboratory II	

In case of Supplementary
Number of Subjects Registered

--

Exam fee amount:

Marks Memo: 30.00

Fine Fee: _____

Total Amount: _____

Certified that the above information is CORRECT and Filled by me.

Submission Date:

Signature of the candidate

Instructions:

- Candidates are instructed to be very careful about the entries to be made in the application.
- All entries should be in candidate's own handwriting.
- Candidate will be held responsible for any incorrect entry that he/she makes.
- Any false or incorrect statement in the application will render the candidate liable to disciplinary action.
- The filled in application should be submitted in the Examination Section.

Office Use Only Fee Receipt Number:

--