

NOTICE

Date: 07-06-2017

Attn: M.TECH II SEMESTER STUDENTS

The Consolidated Internal Marks secured by **M.TECH II SEMESTER (SHIFT1 & SHIFT2)** students are displayed herewith **(also available in the institute website under examinations – Consolidated Internal Marks)**. In case of any discrepancy **(in Marks, Student Name, Father Name, Mother Name, Date of Birth & Gender as per SSC)**, the students are required to report about the same in writing to the under signed on or before **17-06-2017** Requests after this will not be **Considered/Entertained**.



PRINCIPAL

(AUTONOMOUS)

M. TECH II - SEMESTER

BRANCH :- STRUCTURAL ENGINEERING

CONSOLIDATED INTERNAL MARKS

COLLEGE CODE

70

S L · N O ·	H.T.NO.	STUDENT NAME (AS PER SSC)	FATHER NAEM (AS PER SSC)	MOTHER NAME	DATE OF BIRTH	G E N D E R	CC	BC	4	4	4	4	4	4	4	4
									P	P	P	P	P	P	P	P
									T	T	T	T	T	T	T	
									6	6	6	6	6	6	6	
									2	2	2	2	2	2	2	
									1	2	3	4	6	8	C	
1	16701D2001	KOMMADI ABDUL BAVAJI	KOMMADI NAJEER SAHEB	K NAZEEMA BEE	07-12-1994	M	D	20	38	39	37	29	25	36	39	94
2	16701D2002	KADIRI AMARAVATHI	KADIRI SREENIVASULU	K THULASAMMA	10-06-1995	F	D	20	38	40	38	35	26	35	38	87
3	16701D2003	ALAMURI ARAVIND	A THIRUMALAI AH	A VENKATALAKSHMAMMA	12-06-1995	M	D	20	37	33	37	24	27	31	38	86
4	16701D2004	THALAPANENI BABU SARANAM	THALAPANENI GOPAL	T ANUSUYAMMA	24-06-1994	M	D	20	36	37	37	36	29	40	40	94
5	16701D2005	KAKARLA NAGENDRA REDDY	KAKARLA VENKATAKRISHNA REDDY	KAKARLA VENKATA LAKSHUMMA	11-11-1991	M	D	20	33	36	37	31	24	36	37	87
6	16701D2006	BASIREDDY NARENDRA REDDY	BASIREDDY MUNIREDDY	BASIREDDY NARAYANAMMA	01-06-1993	M	D	20	33	26	37	21	24	29	37	87
7	16701D2007	NANDYALA SIVA DAMODHARA REDDY	NANDYALA BOREDDY	N NAGAVENI	10-06-1995	M	D	20	35	32	37	26	23	32	38	85
8	16701D2008	PERIMI SUNEEL	PERIMI SUBRAHMANYAM	PERIMI RAMANAMMA	25-05-1991	M	D	20	34	36	37	31	25	29	39	92
9	16701D2009	CHENDU TEJASWINI	CHENDU NAGA SESHUDU	CHENDU DASTAGIRAMMA	10-06-1995	F	D	20	37	40	38	39	30	34	40	94

4PT621 STRUCTURAL DYNAMICS
 4PT622 FINITE ELEMENT ANALYSIS OF STRUCTURES
 4PT623 STABILITY OF STRUCTURES
 4PT624 ANALYSIS OF SHELLS AND FOLDED PLATES

4PT626 ADVANCED CONCRETE TECHNOLOGY
 4PT628 ADVANCED STEEL DESIGN
 4PT62B ADVANCED CONCRETE TECHNOLOGY LAB - II
 4PT62C SEMINAR - II

Smt. Narayana
 PRINCIPAL

ANNAMACHARYA INSTITUTE OF TECHNOLOGY & SCIENCES, RAJAMPET - 516 126
(AUTONOMOUS)
M.TECH II - SEMESTER
BRANCH :-ELECTRICAL POWER SYSTEMS
CONSOLIDATED INTERNAL MARKS

COLLEGE CODE
70

S L · N O ·	H.T.NO.	STUDENT NAME (AS PER SSC)	FATHER NAEM (AS PER SSC)	MOTHER NAME	DATE OF BIRTH	G E N D E R	CC	BC	4	4	4	4	4	4	4	4
									P	P	P	P	P	P	P	P
1	16701D0701	YEDDULA ANANDA BABU	YEDDULA RAJASEKHAR	YEDDULA OBULAMMA	11-06-1995	M	D	07	29	38	39	32	35	37	95	39
2	16701D0702	PILLAKADAPAM JYOTHEESWAR	PILLAKADAPAM MALLIKARJUNA	PILLAKADAPAM RAMADEVI	10-08-1995	M	D	07	33	31	34	36	32	35	96	33
3	16701D0703	ADAPALA KALYAN KUMAR	ADAPALA PRABHAKAR	A GOWRI	18-04-1993	M	D	07	39	38	39	38	40	38	96	37
4	16701D0704	THOKANCHI NITHIN SAGAR	THOKANCHI SUBBANARASIAH	KATHI BHARATHI	19-08-1993	M	D	07	33	34	36	36	38	35	97	38
5	16701D0705	VASAM VENKATA SRINIVASA RAO	VASAM NARASIMHA RAO	VASAM SANJAMMA	05-06-1995	M	D	07	34	34	36	35	37	37	95	36
6	16701D0706	GANGA VENKATESH	GANGA REDDAIAH	GANGA REDDAMMA	16-07-1994	M	D	07	36	32	36	36	40	35	96	34

4P6221 OPERATION & CONTROL OF POWER SYSTEM
4P6222 ADVANCED POWER SYSTEM ANALYSIS
4P6223 ADVANCED POWER SYSTEM PROTECTION
4P7221 ENERGY CONVERSION SYSTEMS

4P6228 ELECTRICAL POWER DISTRIBUTION AND AUTOMATION
4P6226 MODERN POWER ELECTRONICS
4P7224 SEMINAR - II
4P622B POWER SYSTEM SIMULATION LAB

Som. Narayana
PRINCIPAL

S L · N O ·	H.T.NO.	STUDENT NAME (AS PER SSC)	FATHER NAEM (AS PER SSC)	MOTHER NAME	DATE OF BIRTH	G E N D E R	CC	BC	4	4	4	4	4	4	4	4
									P	P	P	P	P	P	P	P
									5	5	5	5	5	5	5	5
									2	2	2	2	2	2	2	2
									1	2	1	2	3	4	7	8
1	16701D1501	ESA MANOJ KUMAR	ESA RAVI	ESA ANNAPURNA	05-06-1993	M	D	15	39	39	39	39	36	37	86	38

4PE521 ADVANCED OPTIMIZATION TECHNIQUES

4PE522 ROBOTICS

4PF521 MECHANICAL VIBRATIONS

4PF522 MECHANICS OF COMPOSITE MATERIALS

4PF523 THEORY OF PLASTICITY

4PF524 DESIGN FOR MANUFACTURING

4PF527 SEMINAR - II

4PF528 MACHINE DYNAMICS LABORATORY

Sow. Narayana

PRINCIPAL

S L · N O ·	H.T.NO.	STUDENT NAME (AS PER SSC)	FATHER NAME (AS PER SSC)	MOTHER NAME	DATE OF BIRTH	G E N D E R	CC	BC	4	4	4	4	4	4	4	4
									P A 3 2 1	P A 3 2 2	P A 3 2 3	P A 3 2 4	P A 3 2 5	P B 3 2 4	P A 3 2 7	P A 3 2 8
1	16701D3801	ULLANGI JAGADEESWAR	ULLANGI TIRUPALU	ULLANGI INDIRAMMA	11-06-1991	M	D	38	38	33	36	37	38	37	95	39
2	16701D3802	KOGILA MANIKYAMMA	K PULLANNA	K DANAMMA	15-06-1994	F	D	38	38	33	36	30	34	34	92	38
3	16701D3803	GAADI TEJASWINI	GAADI RUDRAREDDY	GAADO SARADA	11-12-1994	F	D	38	39	34	38	32	37	34	95	37
4	16701D3804	DESU VINOD KUMAR REDDY	DESU RAMACHANDRA REDDY	DESU LAXMI NARAYANAMMA	12-07-1995	M	D	38	36	28	34	37	34	33	94	38

4PA321 CODING THEORY AND TECHNIQUES

4PA322 HIGH SPEED NETWORKS

4PA323 MICRO COMPUTER SYSTEM DESIGN

4PA324 DETECTION AND ESTIMATION OF SIGNALS

4PA325 IMAGE AND VIDEO PROCESSING

4PB324 DSP PROCESSORS AND ARCHITECTURES

4PA327 SEMINAR - II

4PA328 ADVANCED COMMUNICATIONS LABORATORY

Smt. Narayana

PRINCIPAL

ANNAMACHARYA INSTITUTE OF TECHNOLOGY & SCIENCES, NEW BOYANAPALLI, RAJAMPET - 516 126
(AUTONOMOUS)
M. TECH II - SEMESTER
BRANCH :- EMBEDDED SYSTEMS
ESS - SHIFT 1
CONSOLIDATED INTERNAL MARKS

COLLEGE CODE 70

S L · N O ·	H.T.NO.	STUDENT NAME (AS PER SSC)	FATHER NAME (AS PER SSC)	MOTHER NAME	DATE OF BIRTH	G E N D E R	C C	B C	4	4	4	4	4	4	4	4
									P	P	P	P	P	P	P	P
									B	B	B	B	B	B	B	B
									3	3	3	3	3	3	3	3
									2	2	2	2	2	2	2	2
									1	2	3	4	5	6	9	A
1	16701D5501	MERUVA HYMAVATHI	MERUVA SIVARAM	M LAKSHUMMA	14-07-1994	F	D	55	34	36	34	35	33	34	94	36
2	16701D5502	GANDLA ISKALA LAKSHMI PRASANNA	G I MOHAN	G I SAVITHRI	05-06-1994	F	D	55	36	34	38	38	33	29	93	37
3	16701D5503	CHEKURI MADHURI	CHEKURI RAVI SANKAR NAIDU	CHEKURI VIJAYA KUMARI	06-08-1994	F	D	55	40	39	40	38	36	38	96	38
4	16701D5504	JAMBULA MOUNIKA	JAMBULA SRINIVASULU	JAMBULA VARALAKSHMI	14-07-1995	F	D	55	30	32	30	34	28	31	95	34
5	16701D5505	MALLEM NAGAMANI	MALLEM THIRUPALU	M SAVITHRAMMA	03-02-1994	F	D	55	33	33	34	37	28	30	95	34
6	16701D5507	ARAVA OBULESU	A SWAMIDASU	A MARIYAMMA	10-05-1995	M	D	55	36	35	31	37	33	28	92	36
7	16701D5508	CHEEKATI REDDY VINEELA	CHEEKATI VENU GOPAL	C SUJATHA	04-03-1990	F	D	55	30	32	32	36	33	32	94	38
8	16701D5509	BOMMIREDDY VENKATA SUBBAMMA	BOMMIREDDY VENKATA SUBBA REDDY	BOMMIREDDY RAMA DEVI	01-01-1995	F	D	55	33	30	34	31	34	33	93	36
9	16701D5510	RAJENDRANGARI VIJAY	R RAJENDRAN	R PARVATHI	05-06-1994	M	D	55	39	39	37	35	36	33	95	36

4PB321 TESTING AND TESTABILITY
4PB322 EMBEDDED SOFTWARE DESIGN
4PB323 HARDWARE SOFTWARE CO - DESIGN
4PB324 DSP PROCESSORS AND ARCHITECTURES

4PB325 MODELLING AND SYNTHESIS THROUGH VERILOG HDL
4PB326 RADIO FREQUENCY IDENTIFICATION
4PB329 SEMINAR - II
4PB32A RTOS & FPGA LABORATORY

Smt. Narayana
PRINCIPAL

ANNAMACHARYA INSTITUTE OF TECHNOLOGY & SCIENCES, NEW BOYANAPALLI, RAJAMPET - 516 126
(AUTONOMOUS)
M. TECH II - SEMESTER
BRANCH :- EMBEDDED SYSTEMS
ESS - SHIFT 2
CONSOLIDATED INTERNAL MARKS

COLLEGE CODE 70

S L · N O ·	H.T.NO.	STUDENT NAME (AS PER SSC)	FATHER NAEM (AS PER SSC)	MOTHER NAME	DATE OF BIRTH	G E N D E R	CC	BC	4	4	4	4	4	4	4	4
									P	P	P	P	P	P	P	P
									B	B	B	B	B	B	B	B
									3	3	3	3	3	3	3	3
									2	2	2	2	2	2	2	2
									1	2	3	4	5	6	9	A
1	16705D5501	SHAIK RAJEENA	SHAIK MASTHANSAHEB	SHAIK FATHIMA	11-11-1991	F	D	55	39	38	38	30	34	37	96	37

4PB321 TESTING AND TESTABILITY
 4PB322 EMBEDDED SOFTWARE DESIGN
 4PB323 HARDWARE SOFTWARE CO - DESIGN
 4PB324 DSP PROCESSORS AND ARCHITECTURES

4PB325 MODELLING AND SYNTHESIS THROUGH VERILOG HDL
 4PB326 RADIO FREQUENCY IDENTIFICATION
 4PB329 SEMINAR - II
 4PB32A RTOS & FPGA LABORATORY


PRINCIPAL

(AUTONOMOUS)

M. TECH II - SEMESTER

BRANCH :- VLSI SYSTEM DESIGN

CONSOLIDATED INTERNAL MARKS

COLLEGE CODE

70

S L · N O ·	H.T.NO.	STUDENT NAME (AS PER SSC)	FATHER NAEM (AS PER SSC)	MOTHER NAME	DATE OF BIRTH	G E N D E R	CC	BC	4	4	4	4	4	4	4	4	4
									P	P	P	P	P	P	P	P	P
									B	C	B	C	C	B	C	C	C
									3	3	3	3	3	3	3	3	3
									2	2	2	2	2	2	2	2	2
									1	1	3	2	3	4	6	7	7
1	16701D5701	SHAIK SALMA	SHAIK MAHABOOB	SHAIK TAHERUNNISA	20-04-1995	F	D	57	31	37	36	29	36	36	88	38	

4PB321 TESTING AND TESTABILITY
 4PC321 LOW POWER VLSI DESIGN
 4PB323 HARDWARE SOFTWARE CO - DESIGN
 4PC322 ASIC DESIGN

4PC323 ALGORITHMS FOR VLSI DESIGN AUTOMATION
 4PB324 DSP PROCESSORS AND ARCHITECTURES
 4PC326 SEMINAR - II
 4PC327 VLSI DESIGN LABORATORY II

Smt. Narayana

PRINCIPAL

ANNAMACHARYA INSTITUTE OF TECHNOLOGY & SCIENCES, RAJAMPET - 516 126
(AUTONOMOUS)
M.TECH II - SEMESTER
BRANCH :- COMPUTER SCIENCE & ENGINEERING
CSE - 1 (SHIFT - 1)
CONSOLIDATED INTERNAL MARKS

COLLEGE CODE 70

S L · N O ·	H.T.NO.	STUDENT NAME (AS PER SSC)	FATHER NAME (AS PER SSC)	MOTHER NAME	DATE OF BIRTH	G E N D E R	CC	BC	4	4	4	4	4	4	4	4
									P	P	P	P	P	P	P	P
1	16701D5801	NAYANAPALLI JYOSHNA	NAYANAPALLI RAJAGOPAL	N JAYAMMA	13-10-1994	F	D	58	38	38	30	36	37	29	93	39
2	16701D5802	KOPPARTHI RAVINDRA REDDY	KOPPARTHI RAMA CHANDRA REDD	KOPPARTHI SREEDEVI	22-06-1994	M	D	58	39	37	36	35	36	32	92	38
3	16701D5803	POLA SAI VAIBHAV	POLA SREENIVAS REDDY	P SIREESHA LATHA	15-03-1994	M	D	58	34	30	30	35	30	29	89	36
4	16701D5804	UDAYAGIRI SAIVENKATATEJA	UDAYAGIRI SRINIVASULU	U SUGUNA	28-02-1995	M	D	58	39	39	39	38	39	39	95	39

4P3121 MANAGING BIG DATA
4P3122 OPEN SYSTEMS FOR WEB TECHNOLOGIES
4P3123 SOFTWARE ARCHITECTURE AND DESIGN PATTERNS
4P3124 DISTRIBUTED OPERATING SYSTEMS

4P3127 CLOUD COMPUTING
4P3128 MOBILE COMPUTING
4P312B SEMINAR - II
4P312C SOFTWARE LABORATORY - II

Smt. Narayana
PRINCIPAL