

NOTICE

DATE: 12-06-2023

REVISED SCHEDULE

SCHEDULE OF III B.TECH II SEMESTER (R20). REGULAR JUNE-2023 END LAB EXAMINATIONS

ELECTRICAL AND ELECTRONICS ENGINEERING

POWER SYSTEM SIMULATION LAB [20A261L]

VENUE: EEE COMPUTER LAB

H.T.No.s	No. of Students	Date	Timings
20701A0201 TO 20701A0229	29	27.06.2023	10:00 a.m. To 01:00 p.m.
20701A0230 TO 20701A0258	29		02.00 p.m. To 05.00 p.m.
20701A0259 TO 20701A0289	30	28.06.2023	10:00 a.m. To 01:00 p.m.
20701A0290 TO 20701A02B8	29		02.00 p.m. To 05.00 p.m.
20701A02B9 TO 20701A02E8	30	30.06.2023	10:00 a.m. To 01:00 p.m.
20701A02E9 TO 20701A02H8	30		02.00 p.m. To 05.00 p.m.
21705A0201 TO 21705A0226	26	25.06.2023	10:00 a.m. To 01:00 p.m.
21705A0227 TO 21705A0254	26		02.00 p.m. To 05.00 p.m.

JAVA PROGRAMMING (20A564L (SO))

H.T.No.	No. of Students	Venue	Timings	Date
20701A0201 To 20701A0239	38	LAB-1	08.30 a.m. To 11.30 a.m.	25-06-2023
20701A0240 To 20701A0279	38		11.30 a.m. To 02.30 p.m.	
20701A0280 To 20701A02B8	38		02.30 p.m. To 05.30 p.m.	
20701A02B9 To 20701A02F5	37	BELL LAB	08.30 a.m. To 11.30 a.m.	26-06-2023
20701A02F6 To 20701A02H8 21705A0201 To 21705A0215	37		11.30 a.m. To 02.30 p.m.	
21705A0216 To 21705A0254	37		02.30 p.m. To 05.30 p.m.	

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

NOTE: External Lab examinations will be conducted only to the eligible candidates who fulfill the academic requirements of the Institute.

Sree. Naray

CHIEF CONTROLLER OF EXAMINATIONS

NOTICE

DATE: 12-06-2023

REVISED SCHEDULE

SCHEDULE OF III B.TECH II SEMESTER (R20). REGULAR JUNE-2023 END LAB EXAMINATIONS

ELECTRICAL AND ELECTRONICS ENGINEERING

MICROPROCESSORS & MICROCONTROLLERS LAB [20A262L] VENUE: MP & MC LAB (PG BLOCK)

H.T.No.s	No. of Students	Date	Timings
20701A0201 TO 20701A0212	12	26.06.2023	09:00 a.m. To 12:00 noon
20701A0213 TO 20701A0224	12		10:00 a.m. To 01:00 p.m.
20701A0225 TO 20701A0236	12		12.00 noon To 03.00 p.m.
20701A0237 TO 20701A0247	11		01.30 p.m. To 04.30 p.m.
20701A0248 TO 20701A0258	11		02.30 p.m. To 05.30 p.m.
20701A0259 TO 20701A0271	12	27.06.2023	09:00 a.m. To 12:00 noon
20701A0272 TO 20701A0283	12		10:00 a.m. To 01:00 p.m.
20701A0284 TO 20701A0295	12		12.00 noon To 03.00 p.m.
20701A0296 TO 20701A02A7	12		01.30 p.m. To 04.30 p.m.
20701A02A8 TO 20701A02B8	11		02.30 p.m. To 05.30 p.m.
20701A02B9 TO 20701A02D0	12	25.06.2023	09:00 a.m. To 12:00 noon
20701A02D1 TO 20701A02E2	12		10:00 a.m. To 01:00 p.m.
20701A02E3 TO 20701A02F4	12		12.00 noon To 03.00 p.m.
20701A02F5 TO 20701A02G6	12		01.30 p.m. To 04.30 p.m.
20701A02G7 TO 20701A02H8	12		02.30 p.m. To 05.30 p.m.
21705A0201 TO 21705A0211	11	28.06.2023	09:00 a.m. To 12:00 noon
21705A0212 TO 21705A0222	11		10:00 a.m. To 01:00 p.m.
21705A0223 TO 21705A0233	10		12.00 noon To 03.00 p.m.
21705A0235 TO 21705A0244	10		01.30 p.m. To 04.30 p.m.
21705A0245 TO 21705A0254	10		02.30 p.m. To 05.30 p.m.

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

NOTE: External Lab examinations will be conducted only to the eligible candidates who fulfill the academic requirements of the Institute.


CHIEF CONTROLLER OF EXAMINATIONS

NOTICE

DATE: 12-06-2023

REVISED SCHEDULE

SCHEDULE OF III B.TECH II SEMESTER (R20). REGULAR JUNE-2023 END LAB EXAMINATIONS

ELECTRICAL AND ELECTRONICS ENGINEERING

POWER SYSTEMS LAB [20A264L]

VENUE: POWER SYSTEMS LAB

H.T.No.s	No. of Students	Date	Timings
20701A0201 TO 20701A0212	12	28.06.2023	09:00 a.m. To 12:00 noon
20701A0213 TO 20701A0224	12		10:00 a.m. To 01:00 p.m.
20701A0225 TO 20701A0236	12		12.00 noon To 03.00 p.m.
20701A0237 TO 20701A0247	11		01.30 p.m. To 04.30 p.m.
20701A0248 TO 20701A0258	11		02.30 p.m. To 05.30 p.m.
20701A0259 TO 20701A0271	12	26.06.2023	09:00 a.m. To 12:00 noon
20701A0272 TO 20701A0283	12		10:00 a.m. To 01:00 p.m.
20701A0284 TO 20701A0295	12		12.00 noon To 03.00 p.m.
20701A0296 TO 20701A02A7	12		01.30 p.m. To 04.30 p.m.
20701A02A8 TO 20701A02B8	11		02.30 p.m. To 05.30 p.m.
20701A02B9 TO 20701A02D0	12	27.06.2023	09:00 a.m. To 12:00 noon
20701A02D1 TO 20701A02E2	12		10:00 a.m. To 01:00 p.m.
20701A02E3 TO 20701A02F4	12		12.00 noon To 03.00 p.m.
20701A02F5 TO 20701A02G6	12		01.30 p.m. To 04.30 p.m.
20701A02G7 TO 20701A02H8	12		02.30 p.m. To 05.30 p.m.
21705A0201 TO 21705A0211	11	30-06.2023	09:00 a.m. To 12:00 noon
21705A0212 TO 21705A0222	11		10:00 a.m. To 01:00 p.m.
21705A0223 TO 21705A0233	10		12.00 noon To 03.00 p.m.
21705A0235 TO 21705A0244	10		01.30 p.m. To 04.30 p.m.
21705A0245 TO 21705A0254	10		02.30 p.m. To 05.30 p.m.

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

NOTE: External Lab examinations will be conducted only to the eligible candidates who fulfill the academic requirements of the Institute.



CHIEF CONTROLLER OF EXAMINATIONS

NOTICE

Date: 12-06-2023

REVISED SCHEDULE

SCHEDULE OF III B.TECH II-SEMESTER (R20) REGULAR JUNE-2023 END LAB EXAMINATIONS

ELECTRONICS AND COMMUNICATION ENGINEERING

EMBEDDED SYSTEMS LAB (20A461L)

H.T.No.	No. of Students	Timings	Venue	Date
20701A0401 To 20701A0417	15	10.00 a.m. To 01.00 p.m.	ECE Computer Lab Side-A (PG Block)	25-06-2023
20701A0418 To 20701A0434	15	11.30 a.m. To 02.30 p.m.		
20701A0435 To 20701A0449	15	01.00 p.m. To 04.00 p.m.		
20701A0450 To 20701A0454 21705A0401 To 21705A0411	15	02.30 p.m. To 05.30 p.m.		
20701A0455 To 20701A0471	16	10.00 a.m. To 01.00 p.m.	ECE Computer Lab Side-B (PG Block)	25-06-2023
20701A0472 To 20701A0487	16	11.30 a.m. To 02.30 p.m.		
20701A0488 To 20701A04A3	15	01.00 p.m. To 04.00 p.m.		
20701A04A4 To 20701A04A8 21705A0412 To 21705A0422	15	02.30 p.m. To 05.30 p.m.		
19701A0490 20701A04A9 To 20701A04C3	16	10.00 a.m. To 01.00 p.m.	ECE Computer Lab Side-A (PG Block)	26-06-2023
20701A04C4 To 20701A04D9	16	11.30 a.m. To 02.30 p.m.		
20701A04E0 To 20701A04F6	16	01.00 p.m. To 04.00 p.m.		
20701A04F7 To 20701A04G7 21705A0423 To 21705A0427	16	02.30 p.m. To 05.30 p.m.		
20701A04G8 To 20701A04I4	16	10.00 a.m. To 01.00 p.m.	ECE Computer Lab Side-B (PG Block)	26-06-2023
20701A04I5 To 20701A04K2	16	11.30 a.m. To 02.30 p.m.		
20701A04K3 To 20701A04L6	12	01.00 p.m. To 04.00 p.m.		
21705A0428 To 21705A0447	18	02.30 p.m. To 05.30 p.m.		

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

NOTE: External Lab examinations will be conducted only to the eligible candidates who fulfill the academic requirements of the Institute.



CHIEF CONTROLLER OF EXAMINATIONS

REVISED SCHEDULE

SCHEDULE OF III B.TECH II-SEMESTER (R20) REGULAR JUNE-2023 END LAB EXAMINATIONS


ELECTRONICS AND COMMUNICATION ENGINEERING

MICROWAVE ENGINEERING LAB (20A462L) VENUE: MOC LAB & COMMUNICATIONS LAB

H.T.No.	No. of Students	Timings	Date
20701A0401 To 20701A0417	15	08.30 a.m. To 11.30 a.m.	28-06-2023
20701A0418 To 20701A0434	15	10.30 a.m. To 01.30 p.m.	
20701A0435 To 20701A0449	15	12.30 p.m. To 03.30 p.m.	
20701A0450 To 20701A0454 21705A0401 To 21705A0411	15	02.30 p.m. To 05.30 p.m.	
20701A0455 To 20701A0471	16	08.30 a.m. To 11.30 a.m.	27-06-2023
20701A0472 To 20701A0487	16	10.30 a.m. To 01.30 p.m.	
20701A0488 To 20701A04A3	15	12.30 p.m. To 03.30 p.m.	
20701A04A4 To 20701A04A8 21705A0412 To 21705A0422 19701A0490	15	02.30 p.m. To 05.30 p.m.	
20701A04A9 To 20701A04C3	16	08.30 a.m. To 11.30 a.m.	27-06-2023
20701A04C4 To 20701A04D9	16	10.30 a.m. To 01.30 p.m.	
20701A04E0 To 20701A04F6	16	12.30 p.m. To 03.30 p.m.	
20701A04F7 To 20701A04G7 21705A0423 To 21705A0427	16	02.30 p.m. To 05.30 p.m.	
20701A04G8 To 20701A04I4	16	08.30 a.m. To 11.30 a.m.	28-06-2023
20701A04I5 To 20701A04K2	16	10.30 a.m. To 01.30 p.m.	
20701A04K3 To 20701A04L6	12	12.30 p.m. To 03.30 p.m.	
21705A0428 To 21705A0447	18	02.30 p.m. To 05.30 p.m.	

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

NOTE: External Lab examinations will be conducted only to the eligible candidates who fulfill the academic requirements of the Institute.


CHIEF CONTROLLER OF EXAMINATIONS

NOTICE

Date: 12-06-2023

REVISED SCHEDULE

SCHEDULE OF III B.TECH II-SEMESTER (R20) REGULAR JUNE-2023 END LAB EXAMINATIONS

ELECTRONICS AND COMMUNICATION ENGINEERING

DIGITAL SIGNAL PROCESSING LAB (20A463L) VENUE: ECE COMPUTE LAB (PG BLOCK)

H.T.No.	No. of Students	Timings	Date
20701A0401 To 20701A0434	30	08.30 a.m. To 11.30 a.m.	27-06-2023
20701A0435 To 20701A0454 21705A0401 To 21705A0411	30	10.00 a.m. To 01.00 p.m.	
20701A0455 To 20701A0486	31	08.30 a.m. To 11.30 a.m.	28-06-2023
20701A0487 To 20701A04A8 21705A0412 To 21705A0422	31	10.00 a.m. To 01.00 p.m.	
19701A0490 20701A04A9 To 20701A04D9	32	01.00 p.m. To 04.00 p.m.	28-06-2023
20701A04E0 To 20701A04G7 21705A0423 To 21705A0427	32	02.30 p.m. To 05.30 p.m.	
20701A04G8 To 20701A04K1	31	01.00 p.m. To 04.00 p.m.	27-06-2023
20701A04K2 To 20701A04L6 21705A0428 To 21705A0447	31	02.30 p.m. To 05.30 p.m.	

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

NOTE: External Lab examinations will be conducted only to the eligible candidates who fulfill the academic requirements of the Institute.


CHIEF CONTROLLER OF EXAMINATIONS

REVISED SCHEDULE

SCHEDULE OF III B.TECH II-SEMESTER (R20) REGULAR JUNE-2023 END LAB EXAMINATIONS


ELECTRONICS AND COMMUNICATION ENGINEERING

JAVA PROGRAMMING (20A564L (SO))

H.T.No.	No. of Students	Venue	Timings	Date
20701A0401 To 20701A0445	41	Neumann Lab	08.30 a.m. To 11.30 a.m.	26-06-2023
20701A0446 To 20701A0454 20701A0455 To 20701A0487	41		11.30 a.m. To 02.30 p.m.	
20701A0488 To 20701A04A8 21705A0401 To 21705A0422	40		02.30 p.m. To 05.30 p.m.	
19701A0490 20701A04A9 To 20701A04E9	42	Neumann Lab	08.30 a.m. To 11.30 a.m.	25-06-2023
20701A04F0 To 20701A04G7 20701A04G8 To 20701A04J4	42		11.30 a.m. To 02.30 p.m.	
20701A04J5 To 20701A04L6 21705A0423 To 21705A0447	42		02.30 p.m. To 05.30 p.m.	

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

NOTE: External Lab examinations will be conducted only to the eligible candidates who fulfill the academic requirements of the Institute.



CHIEF CONTROLLER OF EXAMINATIONS

NOTICE

Date: 12-06-2023

REVISED SCHEDULE

SCHEDULE FOR III B.TECH II-SEMESTER (R20) REGULAR JUNE-2023 END LAB EXAMINATIONS

COMPUTER SCIENCE AND ENGINEERING

(2020 ADMITTED BATCH)

INTERNET OF THINGS AND COMPILER DESIGN LAB (20A565L)

H.T. No.s	No. of Students	Venue	Date	Timings
20701A0501 To 20701A0543	43	IOT & IBM LAB	25-06-2023	08.30 a.m. To 11.30 a.m.
20701A0544 To 20701A0563 20701A0564 To 20701A0589	43			11.30 a.m. To 02.30 p.m.
20701A0590 To 20701A05C0 21705A0501 To 21705A0514	43			02.30 p.m. To 05.30 p.m.
20701A05C1 To 20701A05G4	43	IOT & IBM LAB	26-06-2023	08.30 a.m. To 11.30 a.m.
20701A05G5 To 20701A05H5 20701A05H6 To 20701A05K9	43			11.30 a.m. To 02.30 p.m.
20701A05L0 To 20701A05N3 21705A0515 To 21705A0535	42			02.30 p.m. To 05.30 p.m.

DATA MINING AND SOFTWARE ENGINEERING LAB (20A566L)

H.T. No.s	No. of Students	Venue	Date	Timings
20701A0501 To 20701A0543	43	SILICON LAB	26-06-2023	08.30 a.m. To 11.30 a.m.
20701A0544 To 20701A0563 20701A0564 To 20701A0589	43			11.30 a.m. To 02.30 p.m.
20701A0590 To 20701A05C0 21705A0501 To 21705A0514	43			02.30 p.m. To 05.30 p.m.
20701A05C1 To 20701A05G4	43	SILICON LAB	25-06-2023	08.30 a.m. To 11.30 a.m.
20701A05G5 To 20701A05H5 20701A05H6 To 20701A05K9	43			11.30 a.m. To 02.30 p.m.
20701A05L0 To 20701A05N3 21705A0515 To 21705A0535	42			02.30 p.m. To 05.30 p.m.

Continued.....

NOTICE

Date: 12-06-2023

REVISED SCHEDULE**SCHEDULE FOR III B.TECH II-SEMESTER (R20) REGULAR JUNE-2023 END LAB EXAMINATIONS****COMPUTER SCIENCE AND ENGINEERING****(2020 ADMITTED BATCH)****ADVANCED PYTHON PROGRAMMING LAB (20A567L)**

H.T. No.s	No. of Students	Venue	Date	Timings
20701A0501 To 20701A0543	43	JKC LAB	27-06-2023	08.30 a.m. To 11.30 a.m.
20701A0544 To 20701A0563 20701A0564 To 20701A0589	43			11.30 a.m. To 02.30 p.m.
20701A0590 To 20701A05C0 21705A0501 To 21705A0514	43			02.30 p.m. To 05.30 p.m.
20701A05C1 To 20701A05G4	43	JKC LAB	28-06-2023	08.30 a.m. To 11.30 a.m.
20701A05G5 To 20701A05H5 20701A05H6 To 20701A05K9	43			11.30 a.m. To 02.30 p.m.
20701A05L0 To 20701A05N3 21705A0515 To 21705A0535	42			02.30 p.m. To 05.30 p.m.

PROFESSIONAL COMMUNICATION (20AC61L (SO))

H.T. No.s	No. of Students	Venue	Date	Timings
20701A0501 To 20701A0543	43	Hall No.207,208 (Main Building)	28-06-2023	08.30 a.m. To 11.30 a.m.
20701A0544 To 20701A0563 20701A0564 To 20701A0589	43			11.30 a.m. To 02.30 p.m.
20701A0590 To 20701A05C0 21705A0501 To 21705A0514	43			02.30 p.m. To 05.30 p.m.
20701A05C1 To 20701A05G4	43	Hall No.207,208 (Main Building)	27-06-2023	08.30 a.m. To 11.30 a.m.
20701A05G5 To 20701A05H5 20701A05H6 To 20701A05K9	43			11.30 a.m. To 02.30 p.m.
20701A05L0 To 20701A05N3 21705A0515 To 21705A0535	42			02.30 p.m. To 05.30 p.m.

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

NOTE: External Lab examinations will be conducted only to the eligible candidates who fulfill the academic requirements of the Institute.

Sreed. Waray

CHIEF CONTROLLER OF EXAMINATIONS

REVISED SCHEDULE

SCHEDULE OF III B.TECH II-SEMESTER (R20) REGULAR JUNE-2023 END LAB EXAMINATIONS

ARTIFICIAL INTELLIGENCE AND DATA SCIENCE (2020 ADMITTED BATCH)

AUTOMATA AND COMPILER DESIGN LAB (20A3061L)

VENUE: JKC LAB

H.T. No.s	No. of Students	Date	Timings
20701A3001 To 20701A3028	28	26-06-2023	09.30 a.m. To 12.30 p.m.
20701A3029 To 20701A3051 21705A3001 To 21705A3004	27		01.30 p.m. To 04.30 p.m.

MACHINE LEARNING LAB (20A3062L)

VENUE: SILICON LAB

H.T. No.s	No. of Students	Date	Timings
20701A3001 To 20701A3028	28	28-06-2023	09.30 a.m. To 12.30 p.m.
20701A3029 To 20701A3051 21705A3001 To 21705A3004	27		01.30 p.m. To 04.30 p.m.

BIG DATA ANALYTICS LAB (20A3063L)

VENUE: JKC LAB

H.T. No.s	No. of Students	Date	Timings
20701A3001 To 20701A3028	28	25-06-2023	09.30 a.m. To 12.30 p.m.
20701A3029 To 20701A3051 21705A3001 To 21705A3004	27		01.30 p.m. To 04.30 p.m.

PROFESSIONAL COMMUNICATION (20AC61L (SO))

HALL NO. 212 & 213 (MAIN BUILDING)

H.T. No.s	No. of Students	Date	Timings
20701A3001 To 20701A3028	28	30-06-2023	09.30 a.m. To 12.30 p.m.
20701A3029 To 20701A3051 21705A3001 To 21705A3004	27		01.30 p.m. To 04.30 p.m.

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

NOTE: External Lab examinations will be conducted only to the eligible candidates who fulfill the academic requirements of the Institute.

P. Narayana

CHIEF CONTROLLER OF EXAMINATIONS