Date: 26-11-2024

## SCHEDULE OF III B.TECH I SEMESTER REGULAR (R20) DECEMBER-2024 END LAB EXAMINATIONS CIVIL ENGINEERING

#### (2022 ADMITTED BATCH)

#### **ENVIRONMENTAL ENGINEERING LAB (20A152L)**

**VENUE: EE LAB (CIVIL BLOCK)** 

H.T. No.s	No. of Students	Date	Timings	
22701A0101 To 22701A0106, 23705A0101 To 23705A0122	26	10 10 0004	09.30 a.m. To 12.30 p.m.	
23705A0123 To 23705A0147	25	10-12-2024	01.30 p.m. To 04.30 p.m.	

#### STRUCTURAL ANALYSIS & DESIGN LAB (STAAD PRO) (20A154)

**VENUE: CAD LAB (CE BLOCK)** 

H.T. No.s	No. of Students	Date	Timings	
22701A0101 To 22701A0106, 23705A0101 To 23705A0122	26	11-12-2024	09.30 a.m. To 12.30 p.m.	
23705A0123 To 23705A0147	25	11-12-2024	01.30 p.m. To 04.30 p.m.	

#### JAVA PROGRAMMING (20A555L (SO))

VENUE: CIVIL COMPUTER LAB (CIVIL BLOCK)

H.T. No.s	No. of Students	Date	Timings	
22701A0101 To 22701A0106, 23705A0101 To 23705A0122	26	09-12-2024	09.30 a.m. To 12.30 p.m.	
23705A0123 To 23705A0147	25	09-12-2024	01.30 p.m. To 04.30 p.m.	

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

- NOTE: 1) External Lab examinations will be conducted only to the eligible candidates who fulfill the academic requirements of the Institute.
  - 2) Any clashes with the above schedule are immediately informed to the examination section at least two days before the examination schedule to make alternate arrangements. Otherwise, it will not be entertaining.

CHIEF CONTROLLER OF EXAMINATIONS

Sun- Dane

### ANNAMACHARYA INSTITUTE OF TECHNOLOGY & SCIENCES: RAJAMPET (AUTONOMOUS)

#### NOTICE

Date: 26-11-2024

# SCHEDULE OF III B.TECH I SEMESTER REGULAR (R20) DECEMBER-2024 END LAB EXAMINATIONS ELECTRICAL AND ELECTRONICS ENGINEERING (2022 ADMITTED BATCH)

#### CONTROL SYSTEMS AND SIMULATION LAB (20A251L)

H.T. No.s	No. of Students	Section	Date	Venue	Timings
22701A0201 To 22701A0221	18				09.00 a.m. 12.00 noon
22701A0222 To 22701A0240	19				10.30 a.m. To 01.30 p.m.
22701A0241 To 22701A0252 23705A0202 To 23705A0205, 23705A0218,23705A0219, 23705A0221	19	Sec-A	10-12-2024	1	02.00 p.m. To 05.00 p.m.
21701A0214, 21701A02E1, 21701A02E7 22701A0253 To 22701A0270	19		00 10 0004	Control Systems and Simulation	09.00 a.m. 12.00 noon
22701A0271 To 22701A0290	18	Sec-B	09-12-2024	Lab	10.30 a.m. To 01.30 p.m.
22701A0291 To 22701A0298 23705A0206 To 23705A0215	18				02.00 p.m. To 05.00 p.m.
22701A02A0 To 22701A02C0	21				09.00 a.m. 12.00 noon
22701A02C1 To 22701A02E1	20	Sec-C	11-12-2024		10.30 a.m. To 01.30 p.m.
22701A02E2 To 22701A02E3 23705A0216 To 23705A0236	20	300-0	11-12-2024		02.00 p.m. To 05.00 p.m.

#### POWER ELECTRONICS AND SIMULATION LAB (20A252L)

H.T. No.s	No. of Students	Section	Date	Venue	Timings
22701A0201 To 22701A0221	18				09.00 a.m. 12.00 noon
22701 A0222 To 22701 A0240	19				10.30 a.m. To 01.30 p.m.
22701A0241 To 22701A0252 23705A0202 To 23705A0205, 23705A0218,23705A0219, 23705A0221	19	Sec-A	11-12-2024	Power Electronics and	02.00 p.m. To 05.00 p.m.
21701A0214, 21701A02E1, 21701A02E7 22701A0253 To 22701A0270	19	Sec-B <b>10-12-2024</b>			09.00 a.m. 12.00 noon
22701A0271 To 22701A0290	18		Simulation Lab	10.30 a.m. To 01.30 p.m.	
22701A0291 To 22701A0298 23705A0206 To 23705A0215	18			LUD	02.00 p.m. To 05.00 p.m.
22701A02A0 To 22701A02C0	21	Sec-C			09.00 a.m. 12.00 noon
22701A02C1 To 22701A02E1	20		12-12-2024		10.30 a.m. To 01.30 p.m.
22701A02E2 To 22701A02E3 23705A0216 To 23705A0236	20	300-0	12-12-2024		02.00 p.m. To 05.00 p.m.

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

NOTE: 1) External Lab examinations will be conducted only to the eligible candidates who fulfill the academic requirements of the Institute.

2) Any clashes with the above schedule are immediately informed to the examination section at least two days before the examination schedule to make alternate arrangements. Otherwise, it will not be entertaining.

CHIEF CONTROLLED OF EVANABLATIONS

## ANNAMACHARYA INSTITUTE OF TECHNOLOGY & SCIENCES: RAJAMPET (AUTONOMOUS)

#### NOTICE

Date: 26-11-2024

## SCHEDULE OF III B.TECH I SEMESTER REGULAR (R20) DECEMBER-2024 END LAB EXAMINATIONS ELECTRICAL AND ELECTRONICS ENGINEERING (2022 ADMITTED BATCH)

#### PROFESSIONAL COMMUNICATION (20AC51L (SO))

H.T. No.s	No. of Students	Section	Date	Venue	Timings
22701A0201 To 22701A0231	28			Hall No.	10.00 a.m. To 01.00 p.m.
22701A0232 To 22701A0252 23705A0202 To 23705A0221	28	Sec-A 09-12-2024		302 & 303 (B.Ed., Block)	02.00 p.m. To 05.00 p.m.
21701A0214, 21701A02E1, 21701A02E7 22701A0253 To 22701A0280	28	Sec-B	11-12-2024	Hall No. 302 & 303	10.00 a.m. To 01.00 p.m.
22701A0281 To 22701A0298 23705A0206 To 23705A0215	27		8	(B.Ed., Block)	02.00 p.m. To 05.00 p.m.
22701A02A0 To 22701A02D1	31		Hall No. 09-12-2024 402 & 403 (B.Ed., Block		10.00 a.m. To 01.00 p.m.
22701A02D2 To '22701A02E3 '23705A0216 To 23705A0236	30	Sec-C		402 & 403 (B.Ed., Block)	02.00 p.m. To 05.00 p.m.

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

NOTE: 1) External Lab examinations will be conducted only to the eligible candidates who fulfill the academic requirements of the Institute.

2) Any clashes with the above schedule are immediately informed to the examination section at least two days before the examination schedule to make alternate arrangements. Otherwise, it will not be entertaining.

#### NOTICE

Date: 26-11-2024

## SCHEDULE OF III B.TECH I SEMESTER REGULAR (R20) DECEMBER-2024 END LAB EXAMINATIONS MECHANICAL ENGINEERING

#### (2022 Admitted Batch)

#### THERMAL ENGINEERING LAB (20A351L)

VENUE: Thermal Engineering Lab (ME Block)

H.T. No.s	No. of Students	Date	Timings
22701A0301 To 22701A0309 23705A0301 To 23705A0306	14		09.00 a.m. To 12.00 noon
23705A0307 To 23705A0320	14	09-12-2024	10.30 a.m. To 01.30 p.m.
23705A0321 To 23705A0333	13		01.30 p.m. To 04.30 p.m.
23705A0334 To 23705A0347	13		02.30 p.m. To 05.30 p.m.

#### Machine Tools Lab (20A352L)

Venue: Machine Tools Lab (ME Block)

H.T. No.s	No. of Students	Date	Timings
22701A0301 To 22701A0309 23705A0301 To 23705A0306	14		09.00 a.m. To 12.00 noon
23705A0307 To 23705A0320	14	11-12-2024	10.30 a.m. To 01.30 p.m.
23705A0321 To 23705A0333	13		01.30 p.m. To 04.30 p.m.
23705A0334 To 23705A0347	13		02.30 p.m. To 05.30 p.m.

#### Professional Communication (20AC51L (SO))

Venue: Hall No. 207 & 208 (Main Building)

H.T. No.s	No. of Students	Date	Timings	
22701A0301 To 22701A0309 23705A0301 To 23705A0319	27	10 10 0004	10.00 a.m. To 01.00 p.m.	
23705A0320 To 23705A0347	27	10-12-2024	02.00 p.m. To 05.00 p.m.	

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#### NOTE:

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Date: 26-11-2024

## SCHEDULE OF III B.TECH I-SEMESTER REGULAR (R20) DECEMBER-2024 END LAB EXAMINATIONS ELECTRONICS AND COMMUNICATION ENGINEERING

#### (2022 Admitted Batch)

#### VLSI DESIGN LAB (20A451L)

H.T. No.s	No. of Students	Section	Venue	Date	Timings
<b>21701A0432</b> 22701A0401 To 22701A0421	21				08.00 a.m. To 11.00 a.m.
22701A0422 To 22701A0443	21	Section-A	ECE COMPUTER LAB	09-12-2024	9.00 a.m. To 12.00 noon
22701A0444 To 22701A0453 23705A0401 To 23705A0408 23705A0454 & 23705A0456	20	Section-A	SIDE-A	07-12-2024	10.00 a.m. To 01.00 p.m.
22701A0454 To 22701A0476	22	Section-B	ECE COMPUTER LAB SIDE-B	09-12-2024	08.30 a.m. To 11.30 a.m.
22701A0477 To 22701A0498	21				9.30 a.m. To 12.30 p.m.
22701A0499 To 22701A04A6 23705A0409 To 23705A0427	21				10.30 a.m. To 01.30 p.m.
<b>21701A04D6</b> 22701A04A7 To 22701A04C7	21		ECE COMPUTER	09-12-2024	01.30 p.m. To 04.30 p.m.
22701A04C8 To 22701A04E8	20	Section-C	LAB		02.30 p.m. To 05.30 p.m.
22701A04E9 To 22701A04F5 23705A0415 To 23705A0435	20		SIDE-A		03.15 p.m. To 06.15 p.m.
<b>20701A04I1</b> 22701A04F6 To 22701A04H7	20	Section-D	ECE COMPUTER	09-12-2024	01.00 p.m. To 04.00 p.m.
22701A04H8 To 22701A04J9	20		LAB		02.00 p.m. To 05.00 p.m.
23705A0436 To 23705A0458	20		SIDE-B		03.00 p.m. To 06.00 p.m.

#### MICROPROCESSORS AND INTERFACING LAB (20A453L)

H.T. No.s	No. of Students	Section	Venue	Date	Timings
<b>21701A0432</b> 22701A0401 To 22701A0421	21	Section-A			08.00 a.m. To 11.00 a.m.
22701A0422 To 22701A0443	21		MP&MC LAB	12-12-2024	9.00 a.m. To 12.00 noon
22701A0444 To 22701A0453 23705A0401 To 23705A0408 23705A0454 & 23705A0456	20		MI AMO DAD	12-12-2024	10.00 a.m. To 01.00 p.m.
22701A0454 To 22701A0476	22	Section-B	IC APPLICATIONS	12-12-2024	08.30 a.m. To 11.30 a.m.
22701A0477 To 22701A0498	21				9.30 a.m. To 12.30 p.m.
22701A0499 To 22701A04A6 23705A0409 To 23705A0427	21				10.30 a.m. To 01.30 p.m.
<b>21701A04D6</b> 22701A04A7 To 22701A04C7	21			11-12-2024	01.00 p.m. To 04.00 p.m.
22701A04C8 To 22701A04E8	20	Section-C	MP&MC LAB		02.00 p.m. To 05.00 p.m.
22701A04E9 To 22701A04F5 23705A0415 To 23705A0435	20				03.00 p.m. To 06.00 p.m.
<b>20701A04I1</b> 22701A04F6 To 22701A04H7	20	Section-D		11-12-2024	01.00 p.m. To 04.00 p.m.
22701A04H8 To 22701A04J9	20		IC APPLICATIONS  LAB		02.00 p.m. To 05.00 p.m.
23705A0436 To 23705A0458	20				03.00 p.m. To 06.00 p.m.

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Date: 26-11-2024

## SCHEDULE OF III B.TECH I-SEMESTER REGULAR (R20) DECEMBER-2024 END LAB EXAMINATIONS ELECTRONICS AND COMMUNICATION ENGINEERING

#### (2022 Admitted Batch)

#### PROFESSIONAL COMMUNICATION (20AC51L (SO)) 2 BATCHES

H.T. No.s	No. of Students	Section	Venue	Date	Timings
<b>21701A0432</b> 22701A0401 To 22701A0431	31		Hall No.		10.00 a.m. To 01.00 p.m.
22701A0432 To 22701A0453 23705A0401 To 23705A0408 23705A0454 & 23705A0456	31	Section-A	302 & 303 (B.Ed., Block)	11-12-2024	02.00 p.m. To 05.00 p.m.
22701A0454 To 22701A0487	32	Section-B	Hall No.		10.00 a.m. To 01.00 p.m.
22701A0488 To 22701A04A6 23705A0409 To 23705A0427	32		402 & 403 (B.Ed., Block)	11-12-2024	02.00 p.m. To 05.00 p.m.
<b>21701A04D6</b> 22701A04A7 To 22701A04D8	31	Section-C	Hall No. 302 & 303 (B.Ed., Block)	10-12-2024	10.00 a.m. To 01.00 p.m.
22701A04D9 To 22701A04F5 23705A0415 To 23705A0435	30				02.00 p.m. To 05.00 p.m.
<b>20701A04I1</b> 22701A04F6 To 22701A04I8	30	Saakian D	Hall No.	10-12-2024	10.00 a.m. To 01.00 p.m.
22701A04l9 To 22701A04J9 23705A0436 To 23705A0458	30	Section-D	402 & 403 (B.Ed., Block)	10-12-2024	02.00 p.m. To 05.00 p.m.

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Sale Marie Controller of Examinations

Date: 26-11-2024

# SCHEDULE FOR III B.TECH I-SEM REGULAR (R20) DECEMBER-2024 END LAB EXAMINATIONS COMPUTER SCIENCE AND ENGINEERING (2022 Admitted Batch)

#### **ARTIFICIAL INTELLIGENCE LAB (20A551L)**

H.T. No.s	No. of Students	Section	Venue	Date	Timings
<b>21701A0570</b> 22701A0501 To 22701A0547	46			¥	08.30 a.m. To 11.30 a.m.
22701A0548 To 22701A0594	46	Section A & B	SILICON LAB	06-12-2024	11.30 a.m. To 02.30 p.m.
22701A0595 To 22701A05C2 23705A0501 To 23705A0518	46		,		02.30 p.m. To 05.30 p.m.
22701A05C4 To 22701A05H1	46				08.30 a.m. To 11.30 a.m.
22701A05H2 To 22701A05L9	46	Section C & D	SILICON LAB	07-12-2024	11.30 a.m. To 02.30 p.m.
22701A05M0 To 22701A05P3 23705A0519 To 23705A0534	45				02.30 p.m. To 05.30 p.m.

#### **COMPUTER NETWORKS LAB (20A552L)**

H.T. No.s	No. of Students	Section	Venue	Date	Timings
<b>21701A0570</b> 22701A0501 To 22701A0547	46				08.30 a.m. To 11.30 a.m.
22701A0548 To 22701A0594	46	Section A & B	JKC LAB	07-12-2024	11.30 a.m. To 02.30 p.m.
22701A0595 To 22701A05C2 23705A0501 To 23705A0518	46				02.30 p.m. To 05.30 p.m.
22701A05C4 To 22701A05H1	46				08.30 a.m. To 11.30 a.m.
22701A05H2 To 22701A05L9	46	Section C & D	JKC LAB	06-12-2024	11.30 a.m. To 02.30 p.m.
22701A05M0 To 22701A05P3 23705A0519 To 23705A0534	45				02.30 p.m. To 05.30 p.m.

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Date: 26-11-2024

# SCHEDULE FOR III B.TECH I-SEM REGULAR (R20) DECEMBER-2024 END LAB EXAMINATIONS COMPUTER SCIENCE AND ENGINEERING (2022 Admitted Batch)

#### OBJECT ORIENTED ANALYSIS AND DESIGN (20A554L (SO))

H.T. No.s	No. of Students	Section	Venue	Date	Timings
<b>21701A0570</b> 22701A0501 To 22701A0547	46				08.30 a.m. To 11.30 a.m.
22701A0548 To 22701A0594	46	Section A & B	JKC LAB	09-12-2024	11.30 a.m. To 02.30 p.m.
22701A0595 To 22701A05C2 23705A0501 To 23705A0518	46				02.30 p.m. To 05.30 p.m.
22701A05C4 To 22701A05H1	46				08.30 a.m. To 11.30 a.m.
22701A05H2 To 22701A05L9	46	Section C & D		09-12-2024	11.30 a.m. To 02.30 p.m.
22701 A05M0 To 22701 A05P3	45				02.30 p.m. To 05.30 p.m.

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

NOTE: 1) External Lab examinations will be conducted only to the eligible candidates who fulfill the academic requirements of the Institute.

Date: 26-11-2024

# SCHEDULE FOR III B.TECH I-SEM REGULAR (R20) DECEMBER-2024 END LAB EXAMINATIONS ARTIFICIAL INTELLIGENCE AND DATA SCIENCE (2022 ADMITTED BATCH)

#### DATA MINING AND SOFTWARE ENGINEERING LAB (20A3051L)

H.T. No.s	No. of Students	Venue	Date	Timings
21701A3004,21701A3049 & 21701A3091 22701A3001 To 22701A3039	39	a.		08.30 a.m. To 11.30 a.m.
22701A3040 To 22701A3078	39	NEUMANN LAB	06-12-2024	11.30 a.m. To 02.30 p.m.
22701A3079 To 22701A30A5 23705A3001 To 23705A3012	38			02.30 p.m. To 05.30 p.m.

#### **COMPUTER NETWORKS LAB (20A552L)**

H.T. No.s	No. of Students	Venue	Date	Timings
21701A3004,21701A3049 & 21701A3091 22701A3001 To 22701A3039	39			08.30 a.m. To 11.30 a.m.
22701A3040 To 22701A3078	39	NEUMANN LAB	07-12-2024	11.30 a.m. To 02.30 p.m.
22701A3079 To 22701A30A5 23705A3001 To 23705A3012	38	±		02.30 p.m. To 05.30 p.m.

#### R PROGRAMMING (20A3052L (SO))

H.T. No.s	No. of Students	Venue	Date	Timings
21701A3004,21701A3049 & 21701A3091 22701A3001 To 22701A3039	39			08.30 a.m. To 11.30 a.m.
22701A3040 To 22701A3078	39	SIEMENS LAB	09-12-2024	11.30 a.m. To 02.30 p.m.
22701A3079 To 22701A30A5 23705A3001 To 23705A3012	38			02.30 p.m. To 05.30 p.m.

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

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Otherwise, it will not be entertaining.

Date: 26-11-2024

## SCHEDULE FOR III B.TECH I-SEM REGULAR (R20) DECEMBER-2024 END LAB EXAMINATIONS CSE (ARTIFICIAL INTELLIGENCE)

#### (2022 ADMITTED BATCH)

#### Data Mining and Software Engineering Lab (20A3051L)

H.T. No.s	No. of Students	Venue	Date	Timings
22701A3101 To 22701A3129	28	CIFALENIC		09.30 a.m. To 12.30 p.m.
22701A3130 To 22701A3151 23705A3101 To 23705A3108	27	SIEMENS LAB	06-12-2024	01.30 p.m. To 04.30 p.m.

#### Computer Networks Lab (20A552L)

H.T. No.s	No. of Students	Venue	Date	Timings
22701A3101 To 22701A3129	28	PROJECT &		09.30 a.m. To 12.30 p.m.
22701A3130 To 22701A3151 23705A3101 To 23705A3108	27	RESEARCH LAB (PG BLOCK)	07-12-2024	01.30 p.m. To 04.30 p.m.

#### Professional Communication Lab (20AC51L (SO)) Venue: HALL NO. 301 & 302 (Civil Block)

H.T. No.s	No. of Students	Date	Timings
22701A3101 To 22701A3129	28		10.00 a.m. To 01.00 p.m.
22701A3130 To 22701A3151 23705A3101 To 23705A3108	27	09-12-2024	02.00 p.m. To 05.00 p.m.

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

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Date: 26-11-2024

#### SCHEDULE FOR III B.TECH I-SEM REGULAR (R20) DECEMBER-2024 END LAB EXAMINATIONS

#### **COMPUTER SCIENCE AND ENGINEERING (DATA SCIENCE)**

#### (2022 ADMITTED BATCH)

#### DATA MINING AND SOFTWARE ENGINEERING LAB (20A3051L)

H.T. No.s	No. of Students	Venue	Date	Timings
22701A3201 To 22701A3232	32			09.30 a.m. To 12.30 p.m.
22701A3233 To 22701A3256 23705A3201 To 23705A3207	31	IBM LAB	07-12-2024	01.30 p.m. To 04.30 p.m.

#### **COMPUTER NETWORKS LAB (20A552L)**

H.T. No.s	No. of Students	Venue	Date	Timings
22701A3201 To 22701A3232	32	PROJECT &		09.30 a.m. To 12.30 p.m.
22701A3233 To 22701A3256 23705A3201 To 23705A3207	31	RESEARCH LAB (PG BLOCK)	06-12-2024	01.30 p.m. To 04.30 p.m.

#### OBJECT ORIENTED ANALYSIS AND DESIGN (20A554L (SO))

H.T. No.s	No. of Students	Venue	Date	Timings
22701A3201 To 22701A3232	32			09.30 a.m. To 12.30 p.m.
22701A3233 To 22701A3256 23705A3201 To 23705A3207	31	IBM LAB	09-12-2024	01.30 p.m. To 04.30 p.m.

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

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2) Any clashes with the above schedule are immediately informed to the examination section at least two days before the examination schedule to make alternate arrangements. Otherwise, it will not be entertaining.

CHIEF CONTROLLER OF EXAMINATIONS

Sue. Nava

Date: 26-11-2024

#### SCHEDULE FOR III B.TECH I-SEM REGULAR (R20) DECEMBER-2024 END LAB EXAMINATIONS

#### ARTIFICIAL INTELLIGENCE AND MACHINE LEARNING

#### (2022 ADMITTED BATCH)

#### **COMPUTER NETWORKS LAB (20A552L)**

H.T. No.s	No. of Students	Venue	Date	Timings
22701A3301 To 22701A3337	36	IBM LAB	06-12-2024	08.30 a.m. To 11.30 a.m.
22701A3338 To 22701A3374	36			11.30 a.m. To 02.30 p.m.
22701A3375 To 22701A3395 23705A3301 To 23705A3318	35			02.30 p.m. To 05.30 p.m.

#### DATA MINING LAB (20A3351L)

H.T. No.s	No. of Students	Venue	Date	Timings
22701A3301 To 22701A3337	36	SIEMENS LAB	07-12-2024	08.30 a.m. To 11.30 a.m.
22701A3338 To 22701A3374	36			11.30 a.m. To 02.30 p.m.
22701A3375 To 22701A3395 23705A3301 To 23705A3318	35			02.30 p.m. To 05.30 p.m.

#### R PROGRAMMING LAB (20A3052L (SO))

H.T. No.s	No. of Students	Venue	Date	Timings
22701A3301 To 22701A3337	36	SIEMENS LAB	10-12-2024	08.30 a.m. To 11.30 a.m.
22701A3338 To 22701A3374	36			11.30 a.m. To 02.30 p.m.
22701A3375 To 22701A3395 23705A3301 To 23705A3318	35			02.30 p.m. To 05.30 p.m.

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.

NOTE: 1) External Lab examinations will be conducted only to the eligible candidates who fulfill the academic requirements of the Institute.

2) Any clashes with the above schedule are immediately informed to the examination section at least two days before the examination schedule to make alternate arrangements.
Otherwise, it will not be entertaining.

CHIEF CONTROLLER OF EXAMINATIONS

Jued. Maralf