Hall Ticket Number :
Code: 4G236
II B.Tech. I Semester Supplementary Examinations November 2016 Electrical Engineering and Electronics Engineering
( Common to ME, CSE \& IT )
Max. Marks: 70
Time: 3 Hours
Answer all five units by choosing one question from each unit ( $5 \times 14=70$ Marks )
UNIT-I

1. a) How the Network elements can be classified. Explain it clearly.
b) In the network shown in figure 1b, find all branch currents and voltage drops across all resistors.


Figure 1b

## OR

2. a) Define ohms law and its limitations
b) A current of 10 A flows through a resistor for 10 min . and the power dissipated by the resistor is 100 W . Find the p.d. across the resistor and the energy supplied to the circuit
c) Four resistors of $2 \mathrm{ohm}, 3 \mathrm{ohm}, 4 \mathrm{ohm} \& 5$ ohm respectively, are connected in parallel. What potential difference must be applied to the group in order that total power of 100 W may be absorbed?

## UNIT-II

3. a) Explain the principle of operation of generator
b) A $240 \mathrm{~V}, \mathrm{dc}$ shunt motor takes 32 A of line current of the armature and field resistances are 1.2 and 240 respectively of the load torque remains constant, find the resistance inserted in series with the armature to have the speed.

## OR

4. a) Explain the concept of self- excitation of dc generator and list out the types of generator
b) A $220 \mathrm{~V}, \mathrm{DC}$ shunt motor taker a total current of 100 A and runs at 750 rpm . The resistance of the armature winding and shunt field winding are 0.1 and 40 respectively. Find the torque developed by armature.
UNIT-III
5. a) What is regulation? Derive an expression for the approximate voltage regulation ..... 7M
b) 3-phase, 6 pole, 50 Hz induction motor has a slip of $1 \%$ at no-load and $3 \%$ atfull load. Determine (i) Synchronous speed (ii) No-load speed (iii) Full loadspeed (iv) frequency of motor current at full load7M
OR
6. a) Explain the principle of operation of an alternator with neat diagram ..... 7M
b) A single phase transformer working at unity power factor has an efficiency of $90 \%$ at half load and full load of 500 W . Determine the efficiency at $75 \%$ of full load ..... 7M
UNIT-IV7. a) Explain V-I characteristics of P-N junction diode.7M
b) Write short note on single stage CE amplifier ..... 7M
OR
7. a) Explain the working of P-N-P transistor and mention its input-output characteristics. ..... 7M
b) Explain the operation of half wave and full wave rectifiers with neat circuit diagrams ..... 7M
UNIT-V
8. Derive the expression for electro static deflection of CRO ..... 14M
OR
9. Explain the operation of CRO with a neat sketch. CRO is used to measure frequency and amplitude of a signal. Explain how ..... 14M

## Code: 4G132

II B.Tech. I Semester Supplementary Examinations November 2016
Digital Logic Design
(Common to CSE \& IT)
Time: 3 Hours
Max. Marks: 70
Answer all five units by choosing one question from each unit ( $5 \times 14=70 \mathrm{Marks}$ )

## UNIT-I

1. a) Convert the given Binary number 11011101 into
i) $B C D$
ii) Hexa-decimal
iii) Decimal
b) What is a 2's complement? Explain it with an example? 4 M
c) Explain How to subtract the given two binary numbers using 2's complement with an example.

## OR

2. a) Simplified the following Boolean function. To a minimum no of literals.
i) $x+x^{\prime} y$
ii) $x\left(x^{x}+y\right)$
iii) $x y+x^{\prime} z+y z$. 9M
b) Explain about Sum of Min-terms and Product of Max-terms with suitable examples. 5 M

## UNIT-II

3. a) Simplify the Boolean function
$F(A, B, C, D)=(0,2,5,8,9,13,15)$ and DO-CARE condition $D(A, B, C, D)=(1,7,14)$
b) Implement the above simplified function using NAND and NOR. 5M

OR
4. a) Explain don't-care condition. 4 M
b) Implement AND, OR, and NOT gates using Universal Gates. 6M
c) Describe EX-OR function with an example. 4M

## UNIT-III

5. a) What is a Multiplexer? Explain it. 5M
b) Construct $16 \times 1$ Multiplexer using $4 \times 1$ Multiplexers. 9 M

OR
6. a) What is a combinational Circuit? Explain it. 7M
b) Implement Full adder Combinational Circuit. 7M

UNIT-IV
7. a) What is a Sequential Circuit? 4M
b) Differentiate Latches and Flip-Flops. 4M
c) Describe JK Flip-Flops. 6M

OR
8. Implement 4-bit synchronous counter with a neat diagram.

## UNIT-V

9. Write short notes on
i) ROM
ii) Programmable Logic Array

## OR

10. Explain the following
i) RAM
ii) Error Detection and correction
$\square$
Code: 4G131
II B.Tech. I Semester Supplementary Examinations November 2016

## Advanced Data Structures Through C++

(Common to CSE \& IT)
Max. Marks: 70
Time: 3 Hours
Answer all five units by choosing one question from each unit ( $5 \times 14=70 \mathrm{Marks}$ )

## UNIT-I

1. a) What is a Function? Discuss about various parameter passing methods in C++. 10M
b) Write short notes on Friend Function. 4M

OR
2. a) Explain in detail about Exception Handling Mechanism with an example.
b) Discuss about Dynamic Memory Allocation and De allocation 5M

## UNIT-II

3. a) Differentiate between Constructor Overloading and Function Overloading
b) Discuss in detail about Polymorphism

## OR

4. a) What is inheritance? Explain the different types of inheritance with examples.

10M
b) Write an algorithm for Bubble sort. 4M

## UNIT-III

5. a) What is a Stack ADT? Write the ADT implementation of Stacks in C++. 7M
b) What is a Queue ADT? Explain the various Operations of Queue with an example. 7M

OR
6. a) Explain the various operations of Dictionaries with an example. 10M
b) Compare and contrast between Chaining and Open Addressing. 4M

## UNIT-IV

7. a) What is a Binary Tree? Explain in detail about Binary Trees Traversals with
an example.
b) Construct Max heap and Min Heap for the following data.
$20,15,95,60,35,43,12,75,34,59$

OR
8. a) Explain in detail about Binary Search Trees and its operations. 7M
b) What is an AVL tree? Discuss the various rotations of AVL Trees. 7M

UNIT-V
9. a) Discuss about Splay Trees with an example 8M
b) What is a B-Tree? Explain the various operations of B-Trees. 6 M

OR
10. Explain in detail about ant two Fixed pattern Matching Algorithms 14M

