		Hall Ticket Number :														
		Code: 4G242													R-14	
	Ľ	II B.Tech. II S	eme	este	r Sup	ople	eme	ntar	y Ex	ami	nat	ions	A	Ugu:	st 2021	
					Ele	ectri	ical	Cir	cuit	s-II				Ū		
			(El€	ectri	cal	and	Elec	tron	ics E	ngir	eer	ing)			T . 0.1	
	-	Max. Marks: 70 Answer any five full qu	estic	ons b	y ch	oosir	<u> </u>	ne qi *****	Jestic	on fro	om e	each	U U	nit (5	Time: 3 H ix14 = 70 Mc	
							UNI	T–I								
1.	a)	List out the advantage			-	-			-	-		syster	m.			7M
	b)	Explain the two-wattme	eter r	netho	od of	3-	•		asure	emer	ıt.					7M
2.	a)	A balanced star conne	ected	load	has	an in	OI nned	-	of (8	(4i6	/ph	ase a	an	d sup	nlv voltage i	s
۷.	aj	415 V, 3- supply. Ca					•		•	• •	-			u oup	ply voltage i	7M
	b)	Show that $= \tan^{-1}[3]$	(W₁-	•W ₂) /	/ (W ₁	+W2)] for	3- t	alan	ced I	aggir	ng po)We	er fact	tor load.	7M
	,						UNI	T-11								
3.	a)	State and Prove Initial	value	e the	orem	and	Fina	l valu	e the	orem	۱.					7M
	b)	Explain the step respo	nse c	of ser	ies R	RL Cir	rcuit	using	Lapl	ace ⁻	Trans	sform	۱.			7M
							O	R								
4.	a)	Determine the Laplace	tran				llowir	ng fur	nctior	IS						
		i) 2Cos ² (t)		,	t sin	. ,	5 (1)		2+	•						7M
	b)	Calculate initial & final	i valu	e of i	the fi		.,		-e-sico	os2t						7M
F	c)	Explain the significanc	o of i	nitial	0000			111								5M
5.	a) b)	A series RL circuit with						s a Si	nusoi	dal \	/oltad	ne so	our	ce V(t)=150Sin500	
	D)	Determine the expression				-0.21	That		11000		οπαξ	<i>j</i> e	, and)=10000000	 9M
							O	R								
6.		Derive the expression								uit is	s exc	ited I	by	sinus	soidal voltag	е
		source $V(t) = V_m Sin(t)$	+) v	vhen	swito				:=0							14M
_						L										
7.	a)	Explain all wave form				-	eleva	ant e	xamp	les.						7M
	b)	Discuss properties of F	-ourie	er tra	nstor	ms.	O	D								7M
8.	2)	Illustrate the trigonome	stric F	Fouri	ar ca	rias c			of th	a wa	vefor	m sh		vn in f	ia	
0.	a)			oun	51 30	103 0	лра	131011	or un	c wa	veror	111 311	101	VII III I	ig	
		V (t)														
		1				_										
			T/2		3T/2		(sec)									7M
	b)	Determine Fourier tran	sforn	n of (Jaus				-							7M
•	,			l:4:			UNI									78.4
9.	a) b)	Explain the necessary									ntha	eic				7M
	b)	Synthesize the given in	-				usiil	y cieli	n o nte	u y 5)	mule	313.				
		$Z(S) = \frac{6S}{2}$	3.9	3+	35	-										7M
					_		O	R								
10.		Determine the Cauer	form	اممط	===	مانحما	liona	for '	7(a)	_ 2(s	$x^{2} + 1$)(s ² +	+9))		
				1 0110	i ii ie	aiizdi	UUIS		2(3):		$s(s^2)$	+4)		_		14M
							*	**								

		Hall Ticket Number :	
		Code: 4G244	
		II B.Tech. II Semester Supplementary Examinations August 2021 Linear Control Systems (Electrical and Electronics Engineering)	
		Time: 3 Hou Answer any five full questions by choosing one question from each unit (5x14 = 70 Marks ********	
1.	a)	UNIT-I Define control system. State the difference between closed loop and open control system with examples	7M
	b)	Define transfer function. Find the transfer function of closed loop control system	7M
2.	a)	OR Determine the closed loop transfer function of the given system using block reduction technique.	
		$ \begin{array}{c} R(s) \xrightarrow{+} \otimes \xrightarrow{+} \otimes \xrightarrow{+} G_1 \xrightarrow{-} G_1 \xrightarrow{-} G_2 \xrightarrow{+} \otimes \xrightarrow{+} C(s) \\ \hline & H_1 \xrightarrow{-} & $	
		H ₂	8M
	b)	Describe the block diagram reduction rules with figures.	6M
3.		Derive the time domain specifications of a second order system OR	14M
4.	a)	Derive the response first order system with unit ramp input	7M
	b)	Determine the step, ramp and parabolic error constant of unity feedback control system whose forward path transfer function is given as $G(s) = \frac{K(1+2S)(1+4S)}{S^2(S^2+S+1)}$. Also determine	
		steady state error.	7M
5.	a)	UNIT-III Define stability, asymptotic stability and relative stability.	7M
0.	b)	What are the difficulties in forming Routh array? Explain how to overcome.	7M
6.		OR The characteristic polynomial of a system is $s^7+9s^6+24s^5+24s^4+24s^3+24s^2+23s+15=0$. Determine the location of the roots in s-plane and hence stability of the system.	14M
7.	\sim	UNIT-IV Derive the frequency domain specifications of a second order system	10M
7.	a) b)	List the advantages and disadvantages of Frequency response	4M
	,	OR	
8.	a)	Define GM & PM.	4M
	b)	Explain the procedure to determine the Gain margin and Phase margin from Polar plot.	10M
9.	a)	Derive the expression for the transfer function of a lag-lead compensator.	6M
	b)	Define lag compensator and draw the Pole Zero Plot Also state it's effects.	8M
10.		OR The open-loop transfer function of a unity feedback control system is given	
		by $G(s)H(S) = \frac{K}{S(1+0.2S)}$. Design a suitable compensator such that the system will have	
		$Kv=2$ and $PM = 50^{\circ}$	14M

	Н	all Ticket Number :	-							
	Сс	ode: 4GC41								
		II B.Tech. II Semester Supplementary Examinations August 2021								
	Mathematics-III (Common to EEE & ECE)									
		ax. Marks: 70 nswer any five full questions by choosing one question from each unit (5x14 = 70 Marks) ********								
		UNIT–I								
1.										
	b)	Evaluate $\int_{0}^{1} \frac{x^2}{\sqrt{1-x^5}} dx$ in terms of B function	7M							
		OR								
2.	a)	Find real and imaginary parts $\cot z$	7M							
	b)	Find all the roots of $\sin z = 2$	7M							
		UNIT-II								
3.		Determine P such that the function $f(z) = \frac{1}{2}\log(x^2 + y^2) + i Tan^{-1}\left(\frac{px}{y}\right)$ be an analytic								
		function OR	14M							
4.		Find an analytic function whose real part is $e^{-x} [x \sin y - y \cos y]$	14M							
		UNIT-III								
5.		Evaluate $\int_{c} (y^2 + 2xy) dx + (x^2 - 2xy) dy$ where c is the boundary of the region by								
		$y = x^2$ and $x = y^2$	14M							
		OR								
6.		Expand $Log z$ by Taylor's series about z=1.	14M							
		UNIT-IV								
7.	a)	Find the poles and Residues at each pole $\frac{ze^{z}}{(z-1)^{3}}$	7M							
	b)	Use Residue theorem to find the number of zeros of the polynomial $z^{10} - 6z^7 + 3z^3 + 1$ if								
		z < 1	7M							
		OR								
8.		Evaluate $\int_{c} \frac{e^{2z}}{(z-1)(z-2)} dz$ where c is the circle $ z = 3$	14M							
		UNIT–V								
9.		Find the bilinear Transformation which maps the point (-1, 0, 1) into the points (0, i, 3i). OR	14M							
10.		Find the image of the region in the z-plane between the lines y=0 and $y = \frac{f}{2}$ under the								
		Transformation $w = e^z$.	14M							

Co	ode: 4G346	
	II B.Tech. II Semester Supplementary Examinations August 2021	
	Pulse and Digital Circuits	
	(Electrical and Electronics Engineering)	
	ax. Marks: 70 Time: 3 Hour	
A	nswer any five full questions by choosing one question from each unit (5x14 = 70 Marks))
	UNIT–I	
a)	Discuss the application of Attenuator as a CRO probe	
b)	Define the following:	
2)	i) Linear wave Shaping ii) Lower cutoff frequency iii) Rise time	
	OR	
	A 10 Hz square wave is fed to an amplifier. Calculate and plot the output waveform under	
	the following conditions. The lower 3-dB frequency is a) 0.3 Hz b) 3 Hz c) 30Hz	1
	UNIT–II	
a)	State and Prove the clamping circuit theorem.	
b)	Explain the operation of a two level diode clipper with the help of circuit diagram?	
	OR	
a)	Explain the diode switching times with their neat diagrams	1
b)	Explain piecewise linear characteristics of the diode	
	UNIT–III	
	Draw the circuit diagram of Fixed Bias Bistable Multivibrator and explain its operation with	
	the help of wave forms at base and collector	1
	OR	
	Find the Lower and Upper Threshold voltage for Schmitt trigger circuits with following data.	
	Assume transistors with h_{fe} =30, V_{cc} =12V, R_{c1} =4K, R_{c2} =1K, R_1 =2K, R_s =1K, R_2 =6K, R_e =3K	1
a)	How is linearity corrected through adjustment of the driving waveform for a Current Time Base Generator	
b)	What are the applications of Time Base Generators	
b)	OR	
2)	Illustrate the working principle of Bootstrap time base generator	
a) b)	Explain transistor Miller time base generator with neat diagram	
b)		
- \	UNIT-V	
a)	Explain about unidirectional sampling gate with neat sketch	
b)	Discuss the advantages and disadvantages of Unidirectional sampling gate OR	
a)	Verify the truth table of CMOS NOR gate with neat sketches	1
a) b)	What are the applications of sampling gates?	'