	Ha	II Ticket Number :										]	]	_		
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	Coc	<b>le: 7G345</b> II B.Tech. II Ser	nes	ter S	supa	olen	nent	ary	Exa	min	atio	ns N	1av/.	Jur	ne 2024	
					An	alog	g Ele	ectr	onic	∶s–l	I		,.			
	(Electrical and Electronics Engineering) Max. Marks: 70 Time: 3 Hours														•	
		swer any five full qu	vestic	ons b	by ch	oosiı	ng ol	ne q	uesti	on fr	om e	each	unit	(5x		-
							****	****						•		
1.	2)	What is an IC 2 Li	ot ou	t tha		locci		NIT-		Ivolo	in					7M
1.	a) b)													7 M		
	OR												7 101			
2.											7M					
	b)	Derive the gain of	non	inver	ting	ampl	ifier			-						7M
								IT-I								
3.	a)	Illustrate the opera				•				-		741.				7M
	b)	Illustrate the opera	tion	of Si	ubtra	ctor	circu		ng IC	;741	•					7M
4.	a)	Explain how voltage		n he	con	/orto	d inte		ront	usin	a On	-Am	h			7M
<ul><li>4. a) Explain how voltage can be converted into current using Op-Amp.</li><li>b) Discuss the operation of Adder-Subtractor using Op-Amp.</li></ul>										7M						
							UN	IIT–I	II							
5.	a)	Demonstrate the a	pplic	catior	ns of	Op-A	Amp	Com	para	tor.						7M
b) Illustrate the operation of Schmitt Trigger circuit using IC 741.										7M						
0	`							OR		_						
6.								r.	9M							
	b)	Design an astable	mu		alor		սւրս	. ireq	uenc	у ОГ		Ζ				5M
							UN	IIT–ľ	V							
7.	a)	Discuss how PLL	can b	be us	sed fo	or AN				า.						7M
	b)	Draw and Explain	the c	opera	ation	of So	chmit	t trig	ger u	sing	IC55	55.				7M
								OR								
8.	a)	Discuss how PLL	can b	be us	ed a	s fre	quen	cy tra	ansla	tor.						7M
	b)	How a monostable	e mul	ltivibi	rator	can l	be us	sed a	is mi	ssing	g puls	se de	tector	r? E	Explain.	7M
									•							
0	<b>c</b> )	With holp of poot of	licar		voloi	n tha		IIT-\		20110	tor to	(no ^				714
9.	<ul><li>a) With help of neat diagram explain the operation of counter type ADC.</li><li>b) Illustrate the operation of weighted resistor DAC.</li></ul>										7M 7M					
OR													7 111			
10.	a)	Construct the Inve	rted	R-2F		C an	d ex		in de	etail						7M
	b)	With the help of ne					•				lono	lithic	DAC			7M
							*	* *								

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	Cod	e: 7C2/1										<u></u>	]		R-17	
	Code: 7G241 Il B.Tech. Il Semester Supplementary Examinations May/June 2024												-			
AC Machines – I																
	(Electrical and Electronics Engineering) Max. Marks: 70 Time: 3 Hours															
		wer any five full qu	vestic	ons b	y ch	oosir				on fr	om e	each	unit (ť			
								*****								
1.	. a) Explain the different types of losses in the transformer.													7M		
	b)														7M	
	OR															
2.	a)													7M		
	b) Derive the condition for maximum efficiency in a single phase transformer.												7M			
UNIT-II 3 a) Derive pecessary condition for zero and negative regulation of a transformer													714			
3.	a) b)	Derive necessary condition for zero and negative regulation of a transformer. Describe the experimental test procedure to separate the core losses of a transformer.												7M 7M		
	D)	Describe the expe		ilai l	esi p	1000	uure	OR	spare				5565 0	auc		7M
4.		A 100 KVA lighting transformer has a full load loss of 3 KW, the losses being equally														
		divided between iron and copper. During a day, the transformer operates on full load for														
		3 hours, one half loads for 4 hours, and the output being negligible for the remainder of														
	the day. Calculate the all-day efficiency. 14M														14M	
5.	a)	<b>UNIT–III</b> Write the advantages of a transformer bank of three 1-Phtransformers.											7M			
5.	b)	What is the need of	•											he co	onditions for	<i>T</i> IVI
	)	parallel operating			•				-		-	-				7M
								OR								
6.		A 3-phase, 500 kVA, 6000V/400V, 50Hz, delta-star connected transformer is deliverin										•				
		300 kW, at 0.8 pf lagging to a balanced 3-phase load connected to the LV side with HV side supplied from 6000 V, 3- phase supply. Calculate the line and winding currents in														
		both the sides. Assume the transformer to be ideal.													14M	
							UN	IIT-I	V							
7.	a)	Describe the cons	tructi	on o	f a 3-	phas	se ca	ige ty	/pe ir	nduct	tion r	noto	r with r	neat s	sketch.	8M
	b)	Write the comparis	son b	etwe	en c	age	rotor		slip ı	ing r	otor	with	neat di	agra	ms	6M
	,	-						OR								
8.	a)	Prove that maximum torque developed by the 3-Ph induction motor does not depend on the rotor circuit resistance?														7M
	b)	Explain the phenomenon of crawling and cogging. Also explain it effect.													7M	
9.	a)	Explain the induction	ion m	otor	oper	atior	n unc	ler in	jectio	on of	an e	.m.f.	into th	e rot	or circuit	7M
	b)													7M		
OR 10. A cage induction motor when started by means of a star-delta starter takes 190% of f											100% of full					
10.		load line current a					•									
	torque and current in terms of full load values, if an auto transformer with 80% tapping were employed.															
												14M				