Hall Ticket Number :]			
Code: 1G355				<u> </u>				<u> </u>		<u> </u>		R-11 / R	-13		
III B.Tech. I Semester Supplementary Examinations May 2018															
Microprocessors and Interfacing (Common to CSE & IT)															
Max. Marks: 70 Time: 3 Hours													lours		
Answer any five questions All Questions carry equal marks (14 Marks each) ********															
1. a)		Describe the functionality of the following pins of 8086 microprocessor: <i>i. NM1 ii. HOLD ttt. IO/\overline{M} iv. READY v. ALE</i>													
b) Illustrate the following timing diagrams wr.t 8086 microprocessor:															
i. Minimum mode Memory Read operationii. Maximum mode I/O Write operation													7M		
2. a) Describe the functionality of the following instructions of 8086 microprocessor															
	with the help of its syntax and examples:														
<i>i.LEA ii.CBW iii. IDIV iv. AAA v. JMP</i> b) Develop an 8086 ALP to move a string of 50 data bytes from offset add											offect address	10M			
6)	•	Develop an 8086 ALP to move a string of 50 data bytes from offset address 2000H to offset address 3000h.													
3. a)	3. a) Design an interfacing diagram of interfacing DAC to 8086 μ P through 8255											through 8255			
b)	and then write a program to generate a Triangular waveform. b) What is the importance of 8255 PPI? Interface an 8255 with 8086 so as to have											7M			
,	,													7M	
4. a)	Interface two 8K SRAM chips and two 4K EPROM chips with 8086 so as to form a completely working system configuration. Assume continuous memory map.										7M				
b) What is the advantage of DMA controlled							• •								
	program controlled data transfer? With a neat sketch explain the principle operation of 8257 DMA controller.										7M				
5. a)	Give the funct		•												
b)	write a prograr What is the n		-											9M	
~)		What is the need of 8259 PIC? Briefly explain how the 8259 channels an interrupt request from a peripheral to the 8086 μ P.													
6. a)	6. a) Draw and explain Command word and Mode word formats of 8251 U									1 USART.	8M				
b)	How TTL to RS	6-232	C an	d RS	-232	C to	TTL	conve	ersio	ns ar	e acł	nieve	d? Explain.	6M	
7. a)	List and explai	in the	e ma	jor di	ffere	nces	betv	veen	8028	36 ar	nd 80)386.		6M	
b)	b) List out the salient features of Pentium and Pentium Pro processors.									ors.	8M				
8. a)	Discuss variou	is ad	dres	sing	mode	es of	805 ⁻	1 witl	n exa	mple	es.			10M	
b)	Illustrate the int	Discuss various addressing modes of 8051 with examples. Illustrate the internal and external memory organization w.r.t 8051 microcontroller. ***												4M	