Hall	Tick	et Number :											]		
Code	Code: 4G252														
III B.Tech. I Semester Supplementary Examinations May 2019 Transmission of Electric Power															
										<b>c r</b> a Ingin					
		arks: 70 ver all five uni					e qu			-			5 x 14	Time: 3 Ho = 70 Marks )	Urs
								UNIT	<b>-</b> I	]					
1.	a)	Derive an e line when co	•					•						transmission ansposed.	7N
	b)	are spaced a	at the	e ver	tices	of th								e conductors The diameter	71
		of each cond	JUCIO	oris	I.SCN	1.		OF	•						7N
2.	a)	Derive an ex	nres	sion	for th	ne ca	naci			ወ ሳ	/erhe	ad t	ransmi	ission line	7N
2.	b)	Calculate the wires 12mm	e car in d	oacit iame	ance eter a	of 1 nd s	Φ ov pace	verhe d un	ad li iform	ne co Ily 2.	onsis 5m a	ting apart	of a p . If the	air of parallel line is 30km	,
		charging cur										/sten	n. vvna	at will be the	7N
								JNIT	·	]					
3.	a)	Derive the transmission		•						- T	repre	esent	ted me	edium length	7N
	b)	Resistance/l Susceptance 0.9pf laggin calculate a)	km/pl e/km/ lig at senc	hase /pha 66I ling (	se – 0 se – KV a end v	.1oh 10x <sup>7</sup> at th /oltag	m, lı 10 <sup>-6</sup> r ere geb)	nduc nho. ceivi sen	tive If the ng e ding	react e line end.	ance deli Usin	e/km/ vers ig N	/phase a load omina	he constants. – 0.25ohm. of 20 MW at I π method, ing end pf d)	
		voltage regu	latio	n e) t	trans	miss	ion e		•						7N
4.	اد	Using rigoro		neth	nd d	orivo	the			on fr	or ec	ndin	a and	voltage and	
4.	a)	current for a						evh	5221		5 56	IIIII	y enu	voltage and	7N
	b)	lagging at th	ne re	ceivi	ng e	nd. <sup>-</sup>	The g	gene	ralize	ed co	onsta	ants	of the	MW at 0.8pf transmission he regulation	
		of the line ar					t. Use		ninal					U	7N
5.	a)	Derive an ex levels ii) sup				-			ad lin	es w	hen	i) su	pports	are at equal	7N
	b)	conductor ha	as a f the	cros spe	s-seo cific	ctiona gravi	al are ty of	ea of the	2cm conc	n². Tł lucto	ne te r ma	nsio teria	n in th I is 9.9	upports. The e conductors 98m/cm <sup>3</sup> and	-78
		wind pressu	ieis	1.5K	g/m I	engti	1. Ua	aicuia	ile th	e sa(	y and	u ver	ucai sa	ay.	7N

- 6. a) What is Corona and what are the factors that affect the corona.
  - b) A 3Φ, 220KV, 50Hz transmission line consists of 1.5cm radius conductors spaced 2m apart in equilateral triangular formation. If the temperature is 40° C and atmospheric pressure is 76cm. Calculate the corona loss/km of the line. Take  $m_0 = 0.85$ .

## UNIT-IV

- 7. a) Explain the variation of current and voltage on an overhead line when one end of the line is short circuited and open circuited.
  - b) A cable with surge impedance of 100ohm is terminated in two parallel connected open wire lines having surge impedances of 600 and 1000ohms respectively. If a steep fronted voltage wave of 1000V travels along the cable. Find from the first principles the voltage and current in the cable and open wire lines immediately after the travelling wave has reached the transition point. The line may be assumed to be of infinite length.

## OR

Derive the expression of reflection and refraction coefficient line terminated 8. through a resistance and capacitance 14M

## UNIT-V

- 9. a) Derive the formulae for dielectric stress in an underground cable
  - b) Determine the maximum and minimum stress in the insulation of 33KV, single core cable which has core diameter of 1.5cm and sheath of inside diameter 5cm.

OR

- 10. a) Explain the different types of insulating material that are available for the underground cables 7M
  - b) Derive the expression for the capacitance of single and three core cables. 7M

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7M

7M

7M

7M

7M

7M

Hall Ticket Number :														1	
Code	Code: 4G251				I				I	1	R-14				
III B.Tech. I Semester Supplementary Examinations May 2019															
Electrical Machines-III															
(Electrical & Electronics Engineering)															
Max. Marks: 70 Answer all five units by choosing one question from each unit ( 5 x 14 = 70 Marks ) ********									UIS						
							I	UNIT	<b>-</b> I						
1.	a)	Define the following													
		(i) Pole Pite	ch				(ii) (	Coil S	Span			(iii)	Pitch Fa	actor	
		(iv) Distribu	ition F	acto	r		(v) \	Wind	ing F	actor	•				7M
	b)	Determine pole, 3-pha				tion	and	pole	phas	e gr	oup	sequ	ence fo	or 45-slot, 6-	7M
								OF	R						
2.	a)	Explain abo	ut Spa	ace a	and S	lot ha	armoi	nics i	n alte	ernato	ors.				7M
	b)								•	•			•	e, 3-ø, 50 Hz	
											•			span is 150°	
		third harmo		•	•	e nas	a tu	ndan	nenta	I con	npon	ent o	t 0.12 \	Wb and 20%	7M
				npoi	iont.			JNIT	_11						7 1 1 1
3.	a)	What is ar	air o	nap	line?	Exp	L			n cire	cuit a	and	short c	circuit test is	
-	,	conducted of		• •		•			-1 -	-					7M
	b)	The followir	ig test	resu	ilts w	ere o	btain	ed o	n 6,60	00 V	alter	nator	,		
		,	V <sub>oc</sub> (V)	)	3100	)	490	0	660	0	75	00	8300	)	
		I <sub>f</sub> (A) 16 25 37.5 50 70													
		A field current of 20 A is sufficient to circulate a full load current on short circuit.													
										7M					
								OF	8						
4.	a)	•	•			•		•						n of a salient	714
	<b>L</b> )	pole alterna		•		• ·				•			•		7M
	b)	Describe sli	p test	metr	100 (0	o caic				<sub>q</sub> in s	syncn	rono	us gene	erator.	7M
5.	a)	Explain the	opera	tion (	of alte	ernat		<b>JNIT</b> - infin		ısbar	with	varvi	ina stea	am input.	7M
•	b)	•	•									•	•	us generator	
	2)								-			-		Calculate the	
		synchronizi	ng pov	ver p	er m	echa	nical	degr	ee at	full lo	oad (	).8 pf	lagging	g.	7M
								OF	R						
6.	a)	Explain the	two br	ight	and c	one d	lark n	netho	od of	syncl	hroni	zatio	n of alte	ernators.	7M
	b)	Two identic	al 3-ø	alte	rnato	rs are	e cou	pled	in pa	aralle	l to a	tota	l load c	of 1500kW of	
		•		•							•			$60\Omega$ / phase.	
		-			-				-					the excitation jing. Find the	
		power facto		-							nent	15 40	r layy	jiriy. ⊢iriu trie	7M
		r						- 1- 0							

		UNIT-IV	
7.	a)	Explain the various starting methods of synchronous motor.	7M
	b)	Derive the condition for maximum mechanical power developed by synchronous motor.	7M
		OR	
8.	a)	What is hunting in synchronous motor? Explain how it can be suppressed.	7M
	b)	A 2000 V, 3- phase, star connected synchronous motor has an effective resistance and synchronous reactance of $0.2\Omega$ and $2.2\Omega$ per phase respectively. The input is 800 kW at normal voltage and the induced line emf is 2500 V.	7M
		Calculate the line current and power factor.	7 111
9.	a)	With neat diagram explain the construction and working of variable reluctance	
5.	aj	Stepper Motor.	7M
	b)	Compare the working of ac series motor and universal motor.	7M
		OR	
10.	a)	Draw and explain the torque – speed characteristics of single phase induction motor based on the concept of double field revolving theory.	7M
	b)	A universal series motor has a resistance of $30\Omega$ and an inductance of 0.5 H. When connected to a 250V dc supply and loaded to take 0.8 A it runs at 2000 rpm. Determine the speed, torque and power factor when connected to a 250V,	
		50 Hz, ac supply and loaded to take the same current.	7M
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	На	Il Ticket Number :	
	Cod	e: 4G359 R-14	
		III B.Tech. I Semester Supplementary Examinations May 2019	
		Linear and Digital Integrated Circuits Applications	
		(Electrical and Electronics Engineering)	
	Ma	x. Marks: 70 Answer all five units by choosing one question from each unit ( 5 x 14 = 70 Marks ) *********	
		UNIT–I	
1.	a)	List out the AC characteristics of an op-amp and discuss about them.	7M
	b)	Define the terms, CMRR and input bias current. Describe the techniques used for the measurement of these parameters	7M
2	<b>c</b> )	OR	
2.	a)	What are the three differential amplifier configurations? Compare and contrast these configurations.	7M
	b)	Explain ac analysis of differential amplifier.	7M
	,	UNIT-II	
3.	a)	Design an Astable multivibrator using 555 timer for a frequency of 1 kHz and a duty cycle of	
		70%. Assume C=0.1µf.	7M
	b)	b) Give the block diagram of NE 565 PLL and explain the role of each block. Make circuit	
		connections to track the incoming signal and explain its operation. OR	7M
4.	a)	Draw and explain the operation of Mono stable multivibrator using 555 timer. Derive the	
4.	aj	expression for time delay.	7M
	b)	Draw the dc voltage versus phase difference characteristic of balanced modulator phase	
		detector of a PLL indicating all important regions.	7M
		UNIT–III	
5.	a)	Discuss about successive approximation converter with necessary diagrams	7M
	b)	Explain about R2R ladder type DAC.	7M
		OR	
6.	a)	Explain about the operation of Counter type ADC.	7M
	b)	Explain about the operation of Flash type ADC and Discuss its advantages & Disadvantages	7M
7.	a)	<b>UNIT-IV</b> What are the different factors considered in TTL/CMOS interfacing? Explain.	7M
1.	a) b)	Explain the logic levels and noise margins of TTL	7M
	0)	OR	7 111
8.	a)	Design CMOS NOR gate and analyze its behavior using switch models.	7M
	b)	Discuss the static electrical behavior of CMOS logic circuits.	7M
	,		
9.	a)	Realize 16 input multiplexer using two 8 input multiplexers.	7M
	b)	Implement a 4-bit ripple adder using half-adders/full-adders.	7M
		OR	
10.	a)	Design a BCD-to-excess-3 code converter with a BCD-to-decimal decoder and OR gates.	7M
	b)	Draw the logic diagram of a 4-bit ALU and explain.	7M
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Hall	Tick	et Number :	
Code:	<b>4G</b> 2	R-14	
	I	II B.Tech. I Semester Supplementary Examinations May 2019	
		Power Electronics	
Max	Ma	( Electrical and Electronics Engineering ) rks: 70 Time: 3 Hou	irc
	-	er all five units by choosing one question from each unit ( 5 x 14 = 70 Marks )	13
		*****	
1.		<b>UNIT-I</b> Explain the switching characteristics of thyristor during its turn on and turn off	
		process with clear waveform.	14M
		OR	
2.	a)	Explain the dynamic behaviour of power IGBT with relevant waveforms.	10M
	b)	Describe the significance of RBSOA diagram of a power transistor	4M
0		UNIT-II	
3.		Explain in detail various triggering circuits that can be employed in a thyristor circuit.	14M
		OR	
4.	a)	Explain how selection of particular heat sink affects the average forward current	
	ŗ	rating of a thyristor	4M
	b)	Describe briefly about the different commutation techniques used for the	
	、	commutation of thyristors	6M
	c)	How is the gate of a thyristor protected against overcurrent's and over voltages UNIT-III	4M
5.		With the help of a circuit diagram explain the operation of a dual converter	14M
		OR	
6.		Discuss the effect of source inductance on the performance of single phase full	
		converter. Derive the expression for its output voltage	14M
7.		<b>UNIT-IV</b> Give the classification of choppers based on quadrants; also give their circuit	
7.		diagrams along with a brief description of operation.	14M
		OR	
8.	a)	Explain the various control strategies that are employed in chopper circuits	6M
	b)	Derive the average load current in a step-up chopper	8M
0			
9.		Discuss the functioning of three phase voltage source inverter in 120" operating mode with its waveforms	14M
		OR	
10.	a)	A single phase full wave ac voltage controller feeds a load of R= $20\Omega$ with an	
		input voltage of 230V, 50Hz For a firing angle 45°, calculate (i) rms value of	
		output voltage and (ii) Load power and input power factor	8M
	b)	Discuss the principle of phase control in single phase full wave ac voltage	6M
		regulator with R load.	UIVI