

Hall Ticket Number :

R-14**Code: 4G357**

III B.Tech. I Semester Supplementary Examinations February 2021

Microprocessors & Interfacing
(Computer Science and Engineering)

Max. Marks: 70

Time: 3 Hours

Answer all five units by choosing one question from each unit (5 x 14 = 70 Marks)

Marks	CO	Blooms Level
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UNIT-I

1. a) With the help of timing diagrams ANALYZE the minimum mode of 8086 microprocessor.

8M

K4

- b) Find out Physical address for the following :

i) CS = 4000H, IP= ABC4H

ii) DS = 5000H, SI = 2000H

iii) SS = 90000H, SP=9000H

6M

K3

OR

2. a) Draw the internal block diagram of 8086 microprocessor and ANALYZE the both units

6M

K4

- b) i) Explain the ASSEMBLY directives ORG, DB with examples

ii) Explain the INSTRUCTIONS DAA, ADD with examples

8M

K3

UNIT-II

3. a) Differentiate I/O interfacing methods of 8086 microprocessor.

6M

K2

- b) Explain stepper Motor function and Write a program for stepper motor forward and backward rotation.

8M

K2

OR

4. Explain the Operational Modes of 8255 along with Block Diagram.

14M

K2

UNIT-III

5. a) Distinguish between programmed I/O and Interrupt driven I/O.

6M

K2

- b) With neat sketch explain the architecture of 8259 PIC

8M

K2

OR

6. Explain in detail about the Architecture of 8257 with neat diagram.

14M

K2

UNIT-IV

7. Analyze 8251 USART architecture and interfacing with 8086.

14M

K3

OR

8. Analyze 8253 mode of operations and it's interfacing with 8086.

14M

K3

UNIT-V

9. List the salient features of Pentium and Pentium pro processors.

14M

K2

OR

10. Explain Paging operation in 80386.

14M

K2
