Hall	Tick	et Number :												·	
Code	e: 56	254	<u> </u>		I	<u>l</u>		1	1]]	L		R-15
oout	III B.Tech. I Semester Supplementary Examinations May 2018														
Electrical and Electronics Measurements															
Max	(Electrical and Electronics Engineering) Max. Marks: 70 Time: 3 Hours														
-	Answer all five units by choosing one question from each unit (5 x 14 = 70 Marks)														
								U	NIT-						
1.	a)	Explain abou													
	b)	Explain the e	esser	ntial	featu	res	of inc	licati	•	strun	nents	6			
	OR														
2.	 a) Explain principle of operation of moving iron instrument b) Explain characteristic of DMMO motor 														
	b) Explain characteristic of PMMC meter														
3.	UNIT–II 3. a) Explain advantages and disadvantages of dynamometer wattmeter														
-	b)	•	Ũ					0	-						
	b) Explain errors in dynamometer and wattmeter OR														
4.	a)	Explain indu	ction	type	e of s	ingle	e pha	se e	nergy	/ met	ter w	ith ne	eat diag	gram	
	b)	Explain vario	ous ty	vpes	of po	ower	facto	or me	eters						
								U	NIT-I	I					
5.	a)	Explain the r	metho	od of	fmea	asure	emer	nt of I	nedi	um re	esista	ance			
	b)	Explain abo					-			iit dia	agrai	n an	d nece	essary	derivation
		for measure	ment	of u	nkno	wn r	esist	ance							
6.	2)	Explain the r	nothe	nd of	Floes	of	hara	0 m c	OR othod	forr	noac	urom	oont of	high re	sistanco
0.	a) b)	Explain Ande					•							•	
	5)			Jindg		meat	Juren		NIT-I			milec	cooury	onount	alagram.
7.	a)	Explain the p	orinci	ple c	of op	erati	on of				entio	omete	ər.		
	b)	Explain prac	tical	appli	icatio	ons c	of DC	pote	entior	nete	r				
									OR						
8.	a)	Explain the p	oroce	dure	e for	stand	dardi	zatio	n of <i>i</i>	AC p	oten	tiome	eter.		
	b)	Explain the p	oroce	dure	e for	dete	rmina	ation	of B-	H cu	irve				
									VIT-V	/					
9.	a)	Explain the p		•	•										
	b)	Explain the p	oroce	dure	e to n	neas	sure v	/oltag	-	d cu	rrent	usin	g CRO		
4.0	、								OR						
10.	a)	Explain abou						•	•						
	b)	Explain the p	JUUCI	pie c	л ор	eratio		ram **	р тур	еυν	IVI.				

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Hal	I Ticket Number :											
Code	e: 5G251						R-15					
	III B.Tech. I Semester Supplementary Examinations May 2018											
		E	lectrical	Machines	s-III							
		Electric	cal and Ele	ectronics En	gineering)							
	k. Marks: 70 Answer all five units	by chor	osina ono a	ucstion from	oach unit (/		ne: 3 Hours					
/		by choc	•	******		5 14 - 70	MURS J					
			UNIT	`- I								
1. a)	Explain how the in	duced e	mf is effecte	d by (i) pitch	factor and (ii) distributio	on factor. 7					
b)	The stator of a 3-	-ph, 16-j	pole alterna	tor has 144	slots and 10	conducto	rs per slot					
	connected in two l	ayers an	nd the condu	ctors of eac	h phase are c	onnected i	n series. If					
	the speed of the a		•		•							
	Wb sinusoidally dis	stributed	and if the c	•	50° electrical.		7					
2 2		on for in		OR Dor phono in	a three phas	o oltornoto	r? Montion					
2. a)	Derive an expressi how different wind			• •	•	e allemalo	7 7 Triention					
b)	Find the rms value	•				la 180 slot						
0)	having a single la	•	•	•	•							
	phase groups, eac	•	•	•		•						
	pole=0.025 Wb.						7					
			UNIT	-11								
3. a)	What is Potier Tria	ngle? Ex	xplain its us	e for finding	voltage regula	ition.	7					
b)	The OC and SC te	est result	ts for a 3-ph	50Hz,1500	va ,6600V,Y	-connected	alternator					
	are as follows											
	Field AT/pole	0	3000	6000	16500	20672						
	Voc	0	900	1780	3820	4080						
	lsc	0	65.6	131.22								
	The effective arma		•		•							
	ohms. Estimate the	e ruir ioad	apercentage	OR	ising mmi me	100 at 0.8	pf lagging. 7					
4.	Explain clearly wh	at is me	ant by sync		edance of an	alternator	and how it					
	can be determined		•••	•								
	synchronous impe	•	•		•		•					
	and why?						14					

UNIT–III

- 5. a) Show that in order to obtain a constant voltage, constant frequency of a practical bus bar system, the number of alternators connected in parallel should be as large as possible
 - b) The EMFs of two alternators are 3000∠ 20° V and 2900 ∠0° V. Their synchronous impedances are (2 + j20) /phase and (2.5 + j30) /phase. The load impedance is (10 + j4) /phase. Find the circulating current

OR

- a) What is meant by synchronization? Explain the way of synchronizing an alternator to the infinite bus bars.
 7M
 - b) Derive the expression for load sharing between dissimilar alternators.

7M

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		UNIT-IV	
7.	a)	With the help of a neat Phasor diagram, explain the operation of synchronous motor	7M
	b)	What is hunting in a synchronous motor? Explain how it can be suppressed.	7M
		OR	
8.	a)	Show that the locus of power of a synchronous machine is circle? Give the coordinates of the power circle.	7M
	b)	A 3-phase star connected 440 V; the synchronous motor takes a power input of 5 Kw at rated voltage. Its synchronous reactance is 5 ohms per phase and resistance is negligible. If its excitation voltage is adjusted equal to rated voltage of 400V, compute the load angle, power factor and armature current	7M
		UNIT-V	
9.		Explain about the necessity to provide compensating winding to ac series motor? Also briefly mention different schemes used for compensation? And list the common	4 4 1 4
		applications of AC series motors.	14M
		OR	
10.	a)	Using double field revolving theory, explain the torque-slip characteristics of a single	

phase induction motor and prove that it cannot produce any starting torque

b) What are different types of single phase motors and what are their applications?

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ode: 5		.Tech. I Semester Supplementary Examinations May 2018											
		Environmental Science											
		(Electrical and Electronics Engineering)											
Max. N Ans	-	rs: 70 Time: 3 Hours all five units by choosing one question from each unit (5 x 14 = 70 Marks) ********	S										
		UNIT–I											
1.	a) Environmental science is multi-disciplinary in nature- Justify?												
	b)	How would environmental awareness help to protect the environment?											
		OR											
2.	a)	Outline why public awareness is necessary for environmental protection?	7										
	b)	Evaluate the causes of global environmental issues?	7										
		UNIT–II											
3.	a)	What are renewable resources? Explain them?	7										
	b)	Explain the effects dams on forests and Tribal people?	7										
		OR											
4.	a)	Explain briefly Floods and draughts?	7										
	b)	Explain the impact of traditional agriculture verses modern agriculture on											
		environment?	7										
		UNIT–III											
5.		What is an Ecosystem? Explain the structure and function of a forest ecosystem?	14										
		OR											
6.	a)	Explain											
		i) Food chain	-										
		ii) Food web	7										
	b)	Explain various biodiversity conservation methods?	7										
7	c)	UNIT-IV	7										
7.	a) b)	What are the causes and effects of water pollution?	7										
	b)	Summarize methods of control of thermal pollution?	7										
0	-)	OR Discuss the early relly tion 2	-										
8.	a)	Discuss the soil pollution?	7										
	b)	How can the solid waste be manages?	7										
9.	a)	UNIT-V What are the cause and effects of global warming?	7										
9.	,												
	b)	Explain the salient features of Environmental protection act(1986)?	7										
10	c)	OR What is the impact of population on opvironment?	7										
10.	a) b)	What is the impact of population on environment? What are the measures to be taken to control HIV/AIDS?	7 7										

Hall	Tick	et Number :												
Code	e: 56	R-15												
Couc	III B.Tech. I Semester Supplementary Examinations May 2018													
	Linear and Digital Integrated Circuits Applications													
Мах	. Mo	(Electrical and Electronics Engineering) arks: 70 Time: 3 Hours												
		ver all five units by choosing one question from each unit (5 x 14 = 70 Marks)												
		UNIT-I												
1.	a)	What are the different linear IC packages?												
	b)													
•	OR													
2.	a)	List six characteristics of an ideal op-amp												
	b)	Draw the circuit of a log amplifier using two op-amps and explain its operation.												
2	c)	UNIT-II												
3.	a) b)	What is the major difference between Digital and analog PLLs?												
	b)	Define capture range, lock range and pull-in-time. OR												
4.		-												
4.		Draw the circuit of a Schmitt trigger using 555 timer and explain its operation.												
5.	a)	Describe the various types of electronic switches used in D/A converter.												
	b)	Classify DACs on the basis of their output												
	,	OR												
6.	a)	Explain how dual-slope ADC provides noise rejection.												
	b)	Which is the fastest ADC and why?												
		UNIT-IV												
7.	a)	Explain the concept of sinking and sourcing currents. How are they estimated for CMOS Families.?												
	b)	Draw and explain the circuit of two input CMOS NOR gate.												
		OR												
8.	a)	Discuss the characteristics of CMOS family.												
	b)	What are the sources of stray capacitance in the logic circuit?												
		UNIT–V												
9.	a)	Implement the following function using 3:8 Decoder $F_1=(A,B,C) = m(2,4,6,7)$												
	b)	What do you mean by carry propagation delay?												
		OR												
10.	a)	What is multiplexer? Draw the logic diagram of 8 to 1 line multiplexer.												
	b)	Design full adder using only NOR Gates.												
		ﺎﻟﺪ ﺑﻪ ﺑﻪ												

Hall Ticket Number :															
Code	Code: 5G253									R-15					
Coue	III B.Tech. I Semester Supplementary Examinations May 2018														
Power Electronics															
(Electrical and Electronics Engineering)															
Max. Marks: 70 Answer all five units by choosing one question from each unit (5 x 14 = 70 Marks)												Urs			

	、	– 1 : 4	., ,					UNI			-				
1.	a)	Explain the s		-									_		7M
	b)	What do you method for S		•						•				commutation	7M
								OR	2						
2.	a)	Calculate the branch of a								•			•		
		and current r			•									•	7M
	b)	Why SCRs a associated w		•						•				•	7M
			nur p	aran		oran		UNI			noy				7 101
3.	a)	What are the	reas	sons	for g	lener	ation			 voltac	ies ir	naS	CR cire	cuit? Explain	
	,	the protectio waveforms.								-				•	7M
	b)	Draw RC trig	Inerir	na ciu	rcuit	and e	vnla	nin its	wor	kina	with	relev	ant wa	veforms	7M
	0)		igeni	ig on	oun		SAPIO	OR		ixing	vvitii				7 101
4.	a)	To provide r	eliab	le 'd	v/dť	prote	ectio			CR	used	in a	sinale	e phase fully	
	α,	controlled bi	ridge SCR	rec has	tifier, max	com	npute n 'dv	e the /dt' c	e req capat	uired bility	par of 50	ame)V/µ	ters fo s. The	r a snubber input line to	
		line voltage Assume a da		-				80V a	and t	he so	ource	e ind	uctanc	e is 0.1 mH.	7M
	b)	Why is 'dv/d this rating for		0	•					•	in th	e me	ethod f	or improving	7M
									-	7					
5.	a)	Discuss the power to an waveforms o mode. Also o	RLE of loa	E loa Id vo	d wit Itage	h a and	e pha freev load	ase i vhee d cur	mid-p ling o rent	diode assu	e acr ming	oss i I con	the loa	id. Draw the s conduction	7M
	b)	The load con				-	-		-					consists of a	

b) The load connected to a single phase fully controlled converter consists of a resistance of 5 Ohm, an inductive reactance and a battery of 36 V. The supply voltage is 230 V, 50 Hz. If the thyristor is triggered at a firing angle of 75° and assuming that the current is continuous, determine the average load voltage and average charging current of the battery

7M

- 6. a) Show that in a single phase full wave rectifier feeding power to an RL load, if the thyristor firing angle is made equal to the load impedance angle the load current will be purely sinusoidal. In such case derive the expression for the average value of load current in terms of the circuit parameters. 7M b) Compare a three pulse rectifier with a six pulse rectifier with relevant circuit diagram and waveforms. 7M UNIT-IV 7. a) A Class A chopper circuit is operating on Time Ratio Control (TRC) principle at a frequency of 1 kHz on a 220V dc supply. If the load voltage is 180V, calculate the conducting and blocking period of thyristor in each cycle. 7M 7M b) Draw the circuit of a two quadrant chopper and explain its working. OR 8. a) Compare between Time Ratio Control and Current Limit Control of dc chopper with relevant circuit diagram and waveforms. 7M b) A dc chopper(Class A) circuit connected to a 100V dc source supplies a resistive load of R=5 Ohm in series with an inductance of 40 mH. A freewheeling diode is connected across the load. The load current varies between the limits of 10A and 12A. Compute the time ratio of the chopper 7M UNIT-V 9. a) Explain the operation of single-phase full bridge inverter with relevant circuit diagram and waveforms. 7M b) Explain the operation of single-phase series inverter with relevant circuit diagram and waveforms. What are its limitations? 7M OR 10. a) Explain the operation of single-phase ac voltage controller with neat circuit diagram and output waveforms for R-L load. 7M
 - b) Explain the working of single-phase bridge type cyclo converter with neat circuit diagram and waveforms.
 7M

Hall	Tick	et Number :										
Code:	5G2	R-15										
	I	Il B.Tech. I Semester Supplementary Examinations May 2018 Transmission of Electric Power (Electrical and Electronics Engineering) rks: 70 Time: 3 Hour	rc.									
		er all five units by choosing one question from each unit (5 x 14 = 70 Marks) ********* UNIT-I	5									
1.	a)	Derive an expression for the inductance of a double circuit line whose conductors are placed at the vertices of a regular hexagon.										
	b)	Calculate the inductance per phase of a three-phase double circuit line if the conductors are spaced at the vertices of a hexagon of side 2 m each. The diameter of each conductor is 2.0 cm.										
_		OR										
2.	a)	Derive an expression for the capacitance per km of a single phase line taking into account the effect of ground.	7M									
	b)	Calculate the capacitance per phase of a three-phase three-wire transposed system when the conductors are arranged at the corners of a triangle with sides measuring 1.0m, 1.5m, and 2.0m. Diameter of each conductor is 1.2 cm.	7M									
3.	a)	Derive the expressions for the ABCD constants for the nominal- π circuit of a medium transmission line.	7M									
	b)	A single-phase, 11 kV line with a length of 15 km is to transmit 500 kVA. The inductive reactance of the line is 0.6Ω per km and the resistance is 0.25Ω per km. Calculate the efficiency and regulation for a p.f of 0.75 lag	7M									
		OR										
4.	a)	Explain surge impedance loading in long transmission lines.	4M									
	b)	A three-phase transmission line is 400km long and delivers a load of 350MVA, 0.85p.f lag at 400kV. The ABCD constants of the line are A=D=0.918 \angle 1.5 ⁰ ; B=175 \angle 85 ⁰ ; C =0.0019 \angle 90 ⁰ mhos. Determine the following under full load and no load conditions										
		i) Sending-end line to neutral voltage,ii) The sending-end current, and										
		iii) The percent voltage regulation.	10M									
		UNIT–III										
5.	a)	Explain how the sag of an overhead line can be calculated in case of supports at different levels.	7M									
	b)	Explain the effect of wind and ice loading are taken into account while determining the sag and stress of an overhead line conductor.	7M									
		OR										
6.	a)	Explain various methods for equalizing the potential across the various units in an insulator string.	7M									
	b)	Each conductor of a three phase overhead line is suspended from a cross arm of a steel tower by a string of 4 suspension insulators. The voltage across the second unit is 14.2kV and across the third 20kV. Find the voltage between the conductors and the string efficiency.	7M									

7M

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UNIT–IV

- 7. a) Discuss the phenomenon of wave reflection and refraction. Derive expressions for reflection and refraction coefficients.
 - b) A 200 kV, 3 μs, rectangular surge travels on a line of surge impedance of 400 ohms. The line is terminated in a capacitance of 3000 pf. Find an expression for voltage across the capacitance.

OR

- 8. a) Draw equivalent circuit for finding the transmitted voltage and current surges on a forked line. Derive expressions for the transmitted voltage and currents
 7M
 - b) Two stations are connected together by an underground cable having a surge impedance of 60 ohms joined to an overhead line with a surge impedance of 400 ohms. If a surge having a maximum valve of 100 kV travels along the cable towards the junction with the overhead line, determine the value of the reflected and transmitted wave of voltage and current at the junction

UNIT–V

- 9. a) State the classification of cables (according to voltage) and discuss their general construction.
 - b) A single core lead sheathed cable is graded by using three dielectrics of relative permittivity 5, 4, and 3 respectively. The conductor diameter is 1.8 cm and the inner radius of the sheath is 3 cm. Assuming that all the three dielectrics are worked at the same maximum potential gradient of 40 kV/cm, determine the potential difference in kV between the core and earthed sheath.

OR

- 10. a) Derive an expression for the insulation resistance of a single core metal sheathed cable.
 - b) A 33kV, 3-phase, 2.5 km long feeder consists of single-core cables having a conductor radius of 12mm and an insulation thickness of 8mm. The dielectric has a relative permittivity of 3 and the power factor of the unloaded cable is 0.3. Determine the following
 - (i) capacitance per phase,
 - (ii) charging current per phase,
 - (iii) total charging kVAr,
 - (iv) dielectric per phase and
 - (v) maximum electric stress in the cable

7M
