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MySC 7. a) Define b) List o Techr			OR			
7. a) Define b) List c Techr	establish the con	nmunication	n betwee	en the JAVA program	and Database like	1
b) List o Techr	L? Explain with ste	ep wise suita	able exa	mple		
b) List o Techr		UN	IIT-IV			
Techr	a Cookie? Develo	op a Cookie	program	n using Servlet?		8
		ing Technic	ques? E	xplain at least one	Session Tracking	_
3. a) Illustra	que.					6
	te the usage of ge	atSarvlatCor	OR ofice () an	d getServletInfo () me	thods of a generic	
,	with suitable exar		ing () an		shous of a generic	8
	n Servlet Life Cycl	•				6
			IIT-V			0
9. a) Write	,			eclarations are used	to define methods	
,			u			8
b) List t	·		nts used	I in constructing JSP I	Pages.	6
,	JSP code to demo stance variables.	ction eleme	OR	5	-	
). Write the fol	JSP code to demo stance variables.	ction eleme			VIES table contains	1

Hall	Tick	et Number :	
Cod	e : 50	G151 R-15	
	I	II B.Tech. I Semester Supplementary Examinations May 2018	
		Compiler Design	
		(Computer Science & Engineering)	
-		arks: 70 all five units by choosing one question from each unit (5 x 14 = 70 Mark *****	
		UNIT–I	
1.	a)	Explain the bootstrapping process. What is the advantage of using this process?	7M
	b)	Consider the following fragment of C code: float i, j; i=i*70+j+2;	
		Write the output at all phases of the compiler for the above C code	7M
		OR	
2.	a)	What is the difference between a pass and phase of a compiler	7M
	b)	Write a LEX program for identifying the key words and identifiers from the file	7M
		UNIT–II	
3.	a)	What is top-down parsing? Explain with an example.	7M
	b)	What is backtracking? What is the need of backtracking?	7M
		OR	
4.		Construct predictive parsing table for the grammar $E^{\otimes} E + T, T^{\otimes} TF/F, F^{\otimes} F^*/a/b$	
			14M
5.	a)	UNIT–III Distinguish operator precedence and simple precedence parser	7M
0.	b)	Write note on the specification of YACC	7M
	ω)	OR	
6.	a)	Differentiate between L-attributed and S-attributed grammars	7M
-	b)	Explain how an L-attribute grammar can be converted into a translation scheme	7M
	- /	UNIT-IV	
7.	a)	What is meant by intermediate code generation? Give the benefits of	
		intermediate code generation	7M
	b)	Explain about activation record	7M
		OR	
8.	a)	Explain various methods of three-address code	7M
	b)	Discuss various symbol table organization techniques	7M
		UNIT-V	
9.	a)	Explain DAG and its use. Write the procedure to construct the DAG for statement	7M
	b)	Explain machine dependent code optimization	7M
40	-)	OR	714
10.	a) b)	Explain reducible and non-reducible flow graphs with an example	7M 7M
	b)	Explain the concept of object code forms ***	7M

Hall	Ticl	ket Number :														
Code: 5G152					I			<u> </u>]			R	-15
			Sem	este	ər Su	qqu	lem	ent	ary	Exai	mino	atio	ns N	Лау	2018	
III B.Tech. I Semester Supplementary Examinations May 2018 Computer Networks																
Ma	~ ~ ^ ^	arks: 70			(C	omi	mon	to (CSE a	& IT)				т	imo: 3	3 Hours
-		all five units	by c	hoc	osing	one	e qu	estic	on fro	om e	each	n uni [.]	†(5	-		

1.	a)	Explain the	chara	actei	istics	s of	_	ΝΙΤ- Ν2 Ι		a W	/AN	is re	anir	ed a	nd wh	at
	ω)	Explain the characteristics of WAN? Why a WAN is required and what objectives are achieved by having a WAN									8M					
	b)	Distinguish be	etwee	en W	/ired	and	Wire	less	LAN	s.						6M
	OR															
2.	a)	Compare and	l cont	tract	TCF	P/IP a	and C	OSI r	efere	nce i	mode	els.				7M
	b)	Compare and	l cont	tract	Guio	led T				and V	Virele	ess T	rans	miss	ion.	7M
3.	a)	Discuss the s	liding	. wir	dow	nrot		NIT-l								7M
5.	a) b)	With an exam				•				and	deco	der v	will v	vork.		7M
	2)	Thir ar chai	ipio,	mao	ilato		0110	OR	ouor	ana	autoc		••••••			,
4.	a)	Explain the w	orkin	g of	Carr	ier S	ense	e Mul	tiple	Acce	ess p	rotoc	ol.			9M
	b)	What kinds o	f erro	rs ca	an ar	nd ca	Innot	Vert	ical I	Redu	Indar	icy C	heck	< dete	ermine	5M
							U	IIT–I	II							
5.		Illustrate the is the serious						• •						xamp	ole. Wh	at 14M
								OR								
6.	a)	How Random	n Earl	y Al	gorith	nm h	andle	es th	e Co	nges	tion p	orobl	em.			7M
	b)	Categorize Q				type	of no	etwo	rk ap	plica	tion	and	what	is th	e impa	
		of QoS on Tra	anics	Snap	bing.		111	NIT-I	V							7M
7.	a)	What are the	servi	ces	provi	ded				ort lay	ver?	Expla	ain v	ariou	s the	
	,	methods to	impr		•				·	-						7M
	b)	Explain TCP	propt	ocol	's co	nnec	ction	estal	olishi	ment	and	relea	ase.			7M
								OR								
8.	a)	Why does UE													-	7M
	b)	What is the message forr		of	Bund	lle P	roto	col II	n Ira	ansp	ort L	.ayer	. Ех	plain	with	its 7M
		grin					U	י–דוא	V							
9.	a)	Explain the E	BitTor	rent	Prot	ocol	usec	l in A	pplic	atior	n Lay	er.				7M
	b)	Explain the J	PEG	com	pres	sion	Tech	nniqu	e wit	h ne	at dia	agran	ns.			7M
								OR								
10.	a)	Compare and						•								7M
	b)	Explain the H	.323	arch	ntect	ural		el for ∗∗	Inte	met t	elepi	nony	•			7M
							-n-									

I	Hall	Ticket Number :									
Code: 5G454											
III B.Tech. I Semester Supplementary Examinations May 2018 Data Warehousing and Data Mining											
		(Information Technology) Time: 3 Hours Iswer all five units by choosing one question from each unit (5 x 14 = 70 Marks)									
1.	a)	UNIT–I What is data mining? What are the various steps in the process of knowledge discovery?									
	,	Describe the data mining functionalities and the kind of patterns can be mined?									
	b)	List out major issues in Data mining and explain in detail.	5M								
2.	OR 2. Suppose that the data for analysis includes the attribute age. The age values for the data tuples are 13, 15, 16, 16, 19, 20, 20, 21, 22, 22, 25, 25, 25, 25, 30, 33, 33, 35, 35, 35, 35, 36, 40, 45, 46, 52, 70.										
		 Use smoothing by bin means to smooth the data, using a bin depth of 3. Illustrate your steps. 	6M								
		b. How might you determine outliers in the data?	5M								
		c. What other methods are there for data smoothing?	ЗM								
•	、		1014								
3.	a) b)	What is a Data Warehouse? Construct and explain 3-tier Data Warehouse Architecture?	10M								
	b)	Write a short note on Discovery-Driven Exploration of a Data Cube. OR	4M								
4.	a)	What is strength and weakness of FP in comparison with Apriori?	4M								
	b)	 State Apriori property. Explain Apriori algorithm for discovering frequent itemsets for mining Boolean association rules. 									
_	、										
5.	a)	Briefly explain the Major Steps of Decision Tree Classification.	7M								
	b)	What is pruning? What are the <i>two</i> common approaches to tree pruning? Explain. OR	7M								
6.	2)	What are Bayesian Classifiers?	5M								
0.	a) b)		SIVI								
	6)	methods that can be used to reduce the number of rules generated while still preserving most of the interesting rules.									
7.	a)	UNIT-IV How many types of clustering methods are there? Explain any one partitioning Clustering									
7.	,	algorithm.	7M								
	b)	Explain DBSCAN, density based clustering algorithm to discover cluster with arbitrary shape.	7M								
		OR									
8.	a)	Compare and Contrast Agglomerative Hierarchical Clustering with Divisive Hierarchical Clustering.	8M								
	b)	Write short notes on outlier analysis.	6M								
9.		UNIT-V Identify various Data Mining Applications which useful for analyzing the data for benefit of society.	14M								
OR											
10.	a)	What are the differences between mining techniques of structured data, semi- structured data and unstructured data? Explain.	7M								
	b)	Describe World Wide Web mining. ***	7M								

Hall	Tick	et Number :	
Code	: : 5G	III B.Tech. I Semester Supplementary Examinations May 2018	
		Microprocessors and Interfacing	
		(Common to CSE & IT)	
		arks: 70 Time: 3 Hou for all five write by chaosing one question from each write ($5 \times 14 = 70$ Marke)	Jrs
F	ANSM	ver all five units by choosing one question from each unit (5 x 14 = 70 Marks)	
		UNIT–I	
1.	a)		7N
	b)	Define addressing modes. Explain the following addressing modes with an example.	
		i. Direct Addressing Mode ii. Indexed Addressing Mode iii. Register Indirect Addressing Mode	7N
2.		OR Explain the function of the following signals of 2026	7N
Ζ.	a)	Explain the function of the following signals of 8086. i. ALE ii. READY iii. INTR iv. M/IO	7 1V
	b)	Explain the following assembler directives with an example.	7N
		i. ASSUME ii. EQU iii. PROC iv. DB	
0	-)		
3.	a) b)	Explain different operating modes 8255 PPI.	7№ 7№
	b)	Difference between I/O mapped I/O and Memory mapped I/O.	7 10
4.	a)	Write a program to display the message "HELLO" using 5 seven segment display.	7N
	b)	Interface two 4K X 8 EPROM one chip of 8K X 8 RAM chips with 8086	
		microprocessor. Select suitable maps.	7N
_	,		
5.	a)	What is an advantage of DMA controlled data transfer over interrupt driven or program controlled data transfer? Why DMA controlled data are transfer faster?	7N
	b)	Explain internal block diagram of 8259A.	7N
	~)	OR	
6.	a)	Draw and discuss the architecture of 8257	7N
	b)	What is interrupt service routine? Explain interrupt vector table.	7N
		UNIT–IV	_
7.	a)	Explain 8253/54 internal block diagram.	7N
	b)	Briefly explain architecture of 8251 USRAT.	7N
8.	a)	OR Write a program to transmit 200 bytes of serial data.	7N
	b)	Explain TTL to RS232 and RS232 to TTL conversion with circuits.	7N
	,	UNIT-V	
9.	a)	Explain paging and difference between real and protected mode of segmentation.	7N
	b)	Explain the salient features of 80386 and 80286 processor.	7N
		OR	
10.	a)	What do you mean by a descriptor? Draw and discuss the structure of a general	71
	b)	80286 descriptor. Explain paging mechanism of 80386.	7№ 7№
	D)	***	7 10

На	all Ti	cket Number :							
Cod	le: 5	R-15							
		III B.Tech. I Semester Supplementary Examinations May 2018							
Operating Systems									
Мс	(Computer Science and Engineering) Max. Marks: 70 Time: 3 Hours								
	Ans	wer all five units by choosing one question from each unit (5 x 14 = 70 Marks)							
1.	a)	UNIT-I Illustrate operating systems structure and generation.	5M						
1.	b)	Classify different System Programs.	4M						
	c)	Implement IPC through message queues.	5M						
	0)	OR	0101						
2.	a)	Describe process state diagram and associated queues with a neat diagram	7M						
	b)	Distinguish long term, short term and medium term schedulers.	7M						
		UNIT–II							
3.	a)	Describe thread issues and thread scheduling.	7M						
	b)	What is critical section problem? Explain its requirements.	7M						
Δ	a)	OR Design an algorithm for solving 2-process critical section problem.	7M						
ч.	b)	Summarize atomic transactions.	7M						
	0)		7 1 1 1						
5.	a)	What is safe state? Describe how a safe state ensures deadlock avoidance.	7M						
	b)	Explain paging memory management technique with example. Mention merits							
		and demerits.	7M						
e	c)	OR Evalure the mechanism of demond naging?	7M						
6.	,	Explore the mechanism of demand paging?	7M						
	b)	Explain page replacement algorithms with an example.	7 111						
7.	a)	Write short notes on various Directory structures and their merits, demerits.	7M						
	b)	Explain layered file system structure	7M						
		OR							
8.	a)	Summarize tertiary storage structure	7M						
	b)	What is RAID? Explain various RAID levels.	7M						
		UNIT–V							
9.		Explain how I/O requests are transformed to hardware operations	14M						
10.	a)	OR What are the goals and principles of protection,	7M						
10.	b)	Briefly write about program threats and system threats.	7M						
	~)	***							