III B.Tech. I Semester Supplementary Examinations May 2018
Android Application Development
(Information Technology)

Max. Marks: 70   Time: 3 Hours

Answer *all five* units by choosing one question from each unit (5 x 14 = 70 Marks)

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UNIT–I

1.   Describe anatomy of android application   14M

OR

2. a) Explain types of android application   7M

    b) What is dalvik virtual machine (DVM)   7M

UNIT–II

3.   What are Fragments? How they can be added dynamically?   14M

OR

4.   What is bundle? How can we pass data from one activity to another activity and
        how it receive?   14M

UNIT–III

5. a) What is TextView? and what is downcasting?   7M

    b) From "TextView tv = (TextView)findViewById(R.Id.TextView01)" which is class,
        object and method and how downcasting working here?   7M

OR

6. a) Create custom toast message by using texview programmatically.   4M

    b) Create a program with DatePiker, TimePiker, TextView and button to "When hit
        the button the picked date and time should display in textview" 10M

UNIT–IV

7. a) How to use the built in Content provider?   7M

    b) What is sharedpreferences? Explain it with program for store and load a data? 7M

OR

8.   What is SMS messaging? Create a program to send and receive SMS. 14M

UNIT–V

9.   What is WebView in android? write a program to display javascript code in webview 14M

OR

10 a) Programming zoom in and zoom out of googlemap.   7M

    b) write a program for HTTP connections   7M

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Code: 5G152

R-15

III B.Tech. I Semester Supplementary Examinations May 2018
Computer Networks
(Common to CSE & IT)

Max. Marks: 70   Time: 3 Hours
Answer all five units by choosing one question from each unit (5 x 14 = 70 Marks)
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UNIT–I

1. a) Explain the characteristics of WAN? Why a WAN is required and what objectives are achieved by having a WAN   8M

   b) Distinguish between Wired and Wireless LANs.   6M

OR

2. a) Compare and contract TCP/IP and OSI reference models.   7M

   b) Compare and contract Guided Transmission and Wireless Transmission.   7M

UNIT–II

3. a) Discuss the sliding window protocol in detail.   7M

   b) With an example, illustrate how CRC encoder and decoder will work.   7M

OR

4. a) Explain the working of Carrier Sense Multiple Access protocol.   9M

   b) What kinds of errors can and cannot Vertical Redundancy Check determine 5M

UNIT–III

5. Illustrate the Distance Vector Routing algorithm with a suitable example. What is the serious drawback of Distance Vector Routing algorithm?   14M

OR

6. a) How Random Early Algorithm handles the Congestion problem.   7M

   b) Categorize QoS based on type of network application and what is the impact of QoS on Traffic Shaping.   7M

UNIT–IV

7. a) What are the services provided by the transport layer? Explain various the methods to   improve QoS.   7M

   b) Explain TCP proptocol's connection establishment and release.   7M

OR

8. a) Why does UDP exists? How it identifies the destination entities. Justify 7M

   b) What is the role of Bundle Protocol in Transport Layer. Explain with its message format   7M

UNIT–V

9. a) Explain the BitTorrent Protocol used in Application Layer.   7M

   b) Explain the JPEG compression Technique with neat diagrams.   7M

OR

10. a) Compare and contrast H.323 and SIP protocols   7M

    b) Explain the H.323 architectural model for Internet telephony.   7M

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III B.Tech. I Semester Supplementary Examinations May 2018
Microprocessors and Interfacing
(Common to CSE & I T)

Max. Marks: 70   Time: 3 Hours

Answer *all five* units by choosing one question from each unit (5 x 14 = 70 Marks)

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UNIT–I

1. a) Explain the architecture of 8086 Microprocessor with a neat block diagram.   7M

   b) Define addressing modes. Explain the following addressing modes with an example.

   i. Direct Addressing Mode ii. Indexed Addressing Mode iii. Register Indirect Addressing Mode   7M

OR

2. a) Explain the function of the following signals of 8086.   7M

      i. ALE   ii. READY   iii. INTR   iv. M/IO

   b) Explain the following assembler directives with an example.   7M

      i. ASSUME   ii. EQU   iii. PROC   iv. DB

UNIT–II

3. a) Explain different operating modes 8255 PPI.   7M

   b) Difference between I/O mapped I/O and Memory mapped I/O.   7M

OR

4. a) Write a program to display the message "HELLO" using 5 seven segment display.   7M

   b) Interface two 4K X 8 EPROM one chip of 8K X 8 RAM chips with 8086 microprocessor. Select suitable maps.   7M

UNIT–III

5. a) What is an advantage of DMA controlled data transfer over interrupt driven or program controlled data transfer? Why DMA controlled data are transfer faster?   7M

   b) Explain internal block diagram of 8259A.   7M

OR

6. a) Draw and discuss the architecture of 8257   7M

   b) What is interrupt service routine? Explain interrupt vector table.   7M

UNIT–IV

7. a) Explain 8253/54 internal block diagram.   7M

   b) Briefly explain architecture of 8251 USRAT.   7M

OR

8. a) Write a program to transmit 200 bytes of serial data.   7M

   b) Explain TTL to RS232 and RS232 to TTL conversion with circuits.   7M

UNIT–V

9. a) Explain paging and difference between real and protected mode of segmentation.   7M

   b) Explain the salient features of 80386 and 80286 processor.   7M

OR

10. a) What do you mean by a descriptor? Draw and discuss the structure of a general 80286 descriptor.   7M

   b) Explain paging mechanism of 80386.   7M

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