

Hall Ticket Number :									
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R-15

Code: 5G451

III B.Tech. I Semester Supplementary Examinations August 2021

Android Application Development

(Information Technology)

Max. Marks: 70

Time: 3 Hours

Answer all five units by choosing one question from each unit (5 x 14 = 70 Marks)

UNIT-I

1. a) What is Android? Explain Features and versions of Android? 7M
- b) List out different types of Android Applications? 7M

OR

2. a) Draw various layers of Android Operating System and discuss each layer? 7M
- b) Discuss in detail about Android Development Tools? 7M

UNIT-II

3. Draw activity life cycle architecture and describe the seven methods. 14M

OR

4. Explain the two types of intent with example 14M

UNIT-III

5. Discuss in detail about ViewGroups with an example? 14M

OR

6. a) How to display Pictures using Image Views? 10M
- b) Write a short notes on WEBVIEW? 4M

UNIT-IV

7. Write android program to store and load a file for SD card. 14M

OR

8. Explain in brief about saving and loading User preferences? 14M

UNIT-V

9. Describe the concept of THREADING in android? 14M

OR

10. What is GPS? Create a GPS program with LocationListern interface. 14M

Hall Ticket Number :

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R-15

Code: 5G152

III B.Tech. I Semester Supplementary Examinations August 2021

Computer Networks

(Common to CSE & IT)

Max. Marks: 70

Time: 3 Hours

Answer all five units by choosing one question from each unit (5 x 14 = 70 Marks)

	Marks	CO	Blooms Level
UNIT-I			
1. a) Make a comparison between the TCP/IP and OSI Models.	7M	CO1	L5
b) Explain the spread spectrum and ultra-wideband communications	7M	CO1	L4
OR			
2. a) List and explain the four levels of addressing employed in TCP/IP protocols.	7M	CO1	L3
b) Compare and contrast the fiber optics and copper wire.	7M	CO1	L4
UNIT-II			
3. a) What is the need for framing? Explain different framing methods in Datalink Layer.	7M	CO2	L3
b) Compare Go-Back-N and Selective Repeat sliding window protocols in terms of Storage and Bandwidth requirements to deal with the transmission errors	7M	CO2	L5
OR			
4. a) Consider the delay of pure ALOHA versus slotted ALOHA at low load. Which one is less? Explain your answer.	7M	CO2	L6
b) Define Error Detection and Correction. List and explain the types of errors.	7M	CO2	L3
UNIT-III			
5. a) Compare and contrast the datagram and virtual circuit networks	7M	CO3	L5
b) Explain the Link state routing protocol.	7M	CO3	L3
OR			
6. a) How do you find the distance vector routing algorithm? Discuss.	7M	CO3	L1
b) Draw the format of IPv4 protocol header and explain each field.	7M	CO3	L1
UNIT-IV			
7. a) The following is a dump of a UDP header in hexadecimal format. CB8400D001C001C, Is the packet directed from a client to a server or vice versa?	4M	CO4	L5
b) What are the differences between TCP and UDP? Explain the applications of UDP.	10M	CO4	L2
OR			
8. a) Explain the elements of Transport protocols.	7M	CO4	L4
b) DNS uses UDP instead of TCP. If a DNS packet is lost, there is no automatic recovery. Does this cause a problem, and if so, how is it solved.	7M	CO4	L5
UNIT-V			
9. a) Explain the e-mail architecture and services.	7M	CO5	L3
b) Discuss the Domain Resource Records in detail.	7M	CO5	L2
OR			
10. Explain the following:			
a) User Agent,			
b) Message Formats,			
c) Message Transfer	14M	CO5	L4

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R-15

Code: 5G454

III B.Tech. I Semester Supplementary Examinations August 2021

Dataware Housing and Data Mining

(Information Technology)

Max. Marks: 70

Time: 3 Hours

Answer any five full questions by choosing one question from each unit (5x14 = 70 Marks)

UNIT-I

1. a) Elaborate about various Data Mining Functionalities. 7M
- b) Discuss about the steps involved in Knowledge Discovery from Databases (KDD) process. 7M

OR

2. Describe in detail the various steps and techniques applied for the data preprocessing. 14M

UNIT-II

3. a) Explain with neat sketch, the 3-tier data warehouse architecture. 7M
- b) Describe in detail about the conceptual modeling of data warehouse. 7M

OR

4. a) What are different kinds of Association rules? Explain them. 7M
- b) Explain about Association rule mining to Correlation analysis 7M

UNIT-III

5. a) Briefly specify major steps of Decision tree classification. 7M
- b) How rough set approach and fuzzy set approaches are useful for classification? Explain. 7M

OR

6. a) Explain about Bayesian Classification. 7M
- b) List and describe various methods used for performance evaluation of classifier with example. 7M

UNIT-IV

7. a) Elaborate in detail about partitioning methods. 7M
- b) What are the categories of major clustering methods? Explain. 7M

OR

8. a) Write a detailed note on Density based clustering 7M
- b) Explain about outlier analysis. 7M

UNIT-V

9. a) Elaborate in detail about mining Spatial databases. 7M
- b) Discuss about multidimensional analysis and descriptive mining of complex data objects. 7M

OR

10. a) Explain text data analysis and information retrieval. 7M
- b) Explain Time Series and Sequence Data Mining. 7M

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R-15

Code: 5G356

III B.Tech. I Semester Supplementary Examinations August 2021

Microprocessors & Interfacing

(Common to CSE & IT)

Max. Marks: 70

Time: 3 Hours

Answer all five units by choosing one question from each unit (5 x 14 = 70 Marks)

UNIT-I

- | | Marks | CO | Blooms Level |
|--|-------|----|--------------|
| 1. a) Explain the instructions ADD, AND, SHR, MOVS with examples | 8M | | K2 |
| b) Explain MACRO and MACRO within MACRO with example | 6M | | K3 |

OR

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|---|----|--|----|
| 2. a) i) Explain the pipe lining concept in 8086 | 2M | | |
| ii) Code Segment Physical Address is 78965H. Find out CS and IP value | 4M | | K3 |
| b) Write a procedure to add two numbers using 8086 assembly language | 8M | | K3 |

UNIT-II

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|---|----|--|----|
| 3. a) Compare and Contrast Memory Mapped I/O and I/O mapped I/O | 7M | | K2 |
| b) Interface two 4K X 4 EPROMs and two 4K X 4 RAM chips with 8086 microprocessor. Select suitable map | 7M | | K3 |

OR

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|---|----|--|----|
| 4. a) Compare and Contrast I/O mapped I/O and Memory mapped I/O | 7M | | K2 |
| b) Explain A/D and D/A Converters | 7M | | K2 |

UNIT-III

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|---|-----|--|----|
| 5. a) Explain in detail about the Architecture of 8257 with neat diagram. | 14M | | K2 |
|---|-----|--|----|

OR

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|--|----|--|----|
| 6. a) Explain The Cascading of Interrupt Controllers | 8M | | K2 |
| b) Compare and Contrast Programmed I/O and Interrupted I/O | 6M | | K2 |

UNIT-IV

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|--|----|--|----|
| 7. a) Explain RS-232C Serial Data Standard and 20ma Current loop | 7M | | K2 |
| b) Compare and Contrast Asynchronous and synchronous data transfer methods | 7M | | K2 |

OR

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|---|-----|--|----|
| 8. a) Analyze 8251 USART architecture and interfacing with 8086 | 14M | | K2 |
|---|-----|--|----|

UNIT-V

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|---|-----|--|----|
| 9. a) What are the differences between 8086 and 80286 | 4M | | K2 |
| b) Explain segmentation in 80386 | 10M | | K2 |

OR

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|---|----|--|----|
| 10. a) What are salient features of Pentium pro processor | 6M | | K2 |
| b) Explain real mode of 80386 | 8M | | K2 |
