Hall	Ficke	et Number :	
Code:	5G2	R-1	5
	002	III B.Tech. II Semester Regular Examinations May 2018	
		Microprocessors and Microcontrollers	
Max.	Mar	(Electrical & Electronics Engineering) ks: 70 Time: 3	Hours
	-	er all five units by choosing one question from each unit ($5 \times 14 = 70$ Mark	
		UNIT–I	
1.	a)	Describe the Pin description and illustrate the Pin diagram of 8086µP.	7M
	b)	Distinguish Minimum and maximum mode of operations of 8086µP.	7M
		OR	
2.	a)	Explain about data related addressing modes with examples	8M
	b)	Explain Macros with an example.	6M
		UNIT–II	
3.	a)	Explain the control word register of 8255 PPI.	6M
	b)	Describe the interface of A/D Converter using 8255 with 8086 μ P.	8M
		OR	
4.		Explain the I/O Interfacing methods of 8086 μ P.	14M
		UNIT–III	
5.	a)	Illustrate the basic structure of SRAM and DRAM cells	6M
	b)	Need for DMA? Explain the master and slave modes of DMA.	8M
		OR	
6.	a)	Explain about Memory interfacing to 8086 with an example.	7M
	b)	Explain about architecture of 8257.	7M
		UNIT–IV	
7.	a)	Explain about Interrupt structure of 8086	6M
	b)	Describe TTL to RS232C and RS232C to TTL conversion.	8M
		OR	
8.	a)	Describe 8251 USART architecture.	8M
	b)	Describe the signals of Serial communication standard-RS-232C.	6M
		UNIT–V	
9.	a)	Describe the instruction set of 8051μ C.	8M
	b)	List out the special function registers of 8051µC.	6M
	-	OR	
10.	a)	Draw the pin diagram 8051µC.	6M
	b)	Describe the addressing modes 8051µC.	8M

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Hall Tick	et Number :										[
Code: 50	GA61										R-15	
	III B.Tech. I	l Seme	ester	Reg	gular	Exar	nina	tions	May	y 201	8	
		Μ	ana	gen	nent	Scie	ence					
	(1arks: 70	Electric	cal &	Elec	stroni	cs En	gine	ering)	-		
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					*****		_					,
						IIT-I						
1.	Define Manage	ement?	What	are t			s of M	anage	ement	? Exp	plain?	14N
					0	R						
2. a)	Evaluate Line a		· ·		tion?							71
b)	Evaluate Matrix	Organi	sation	1?								71
2	What is Bradua	tion 2 1	/hot o	ra th		IT-II		ductiv		Typloir	- 0	1 1 1
3.	What is Produc		vnat a	ie in	e meu O			auctiv	ily? c	zxpiair	1?	14N
4.	What is Market	ina and	hriafh		_		ctions	ofma	arkatir	na?		14N
т.	What is Market	ing and	briefly	Слр			510113	Of the	incen	ig:		1 - 11
					UN	IT-III						
5.	Define selection	n? Wha	at are	the o			hniqu	es of	selec	tion flo	owed by a	
	personnel mana						•				•	14N
					0	R						
6.	What is Training	g? Brie	fly exp	olain	the di	fferen	t metł	nods c	of trair	ning?		14N
						T–IV						
7.	Define Financial	Manage	ment?	Ехр	lain the	e objec	ctives	of Fina	ancial I	Manag	ement?	14N
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8	The following d the probability				•	•			•		•	
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SI. NO.	1	2	3	4	5	6	1	8	9
Activity	1-2	1-3	2-4	3-4	4-5	5-6	3-5	5-7	6-7
te	6	8	7	12	3	5	7	11	10

UNIT-V

9. Give a brief note on Total Quality Management. State the main elements of TQM? 14M

OR

10. What is Just-In-Time? Explain its features, advantages and disadvantages? 14M

14M

Hall ⁻	Tick	et Number :	
Code	e: 50	R-15	
Couc		III B.Tech. II Semester Regular Examinations May 2018	
		Object Oriented Programming Concepts	
May		(Electrical and Electronics Engineering) arks: 70 Time: 3 Hou	irc
		ver all five units by choosing one question from each unit (5 x 14 = 70 Marks) *********	12
		UNIT–I	
1.	a)		714
	b)	benefits of OOP over structured programming. Explain passing reference variables as arguments to functions.	7M 7M
	5)	OR	7 111
2.	a)	Compare Object based programming and Object Oriented Programming.	
	,	Explain how data and functions are organized in OOP.	7M
	b)	Mention the different types of constructors. Elaborate parameterized	
		constructors through an example program.	7M
3.	a)		
	,	writing a C++ program	8M
	b)	Discuss the following C++ concepts with suitable examples.	
		i. friend function	
		ii. this pointer	6M
4.	a)	OR Define Function Overloading. Write a C++ program to demonstrate Operator	
ч.	aj	Overloading.	7M
	b)	Write a function using reference variables as arguments to swap the values of	
		pair of integers.	7M
5.	a)	UNIT-III List at least five major C++ features that were intentionally removed from	
0.	u)	Java. Explain how is Java more secure than other languages?	7M
	b)	Mention the five types of tokens in Java. Explain the derived data types in Java.	7M
		OR	
6.	a)	Write a program to find the number of and sum of all integers greater than	714
	b)	100 and less than 200 that are divisible by 5. Write a program in Java to print first m Fibonacci series using while loop.	7M 7M
	5)	UNIT-IV	7 111
7.	a)	Write about the frequently used Java API packages. Write a program to	
		import classes from package 1 to package 2.	7M
	b)	Write the steps involved in adding a class to a package.	7M
8.	a)	OR Discuss the process of throwing own exceptions in Java.	7M
0.	a) b)	Explain various keywords in exception handling with Java.	7M
	-)		
9.	a)	What is a Thread? How are threads created?	7M
	b)	Write an example program to create threads using Thread class.	7M
40	-)	OR Evalain the life evale of enalst	014
10.	a) b)	Explain the life cycle of applet. Demonstrate the creation of an applet using an example program.	8M 6M
	D)		

Hall Ticket Number :												r
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Code: 5G261

III B.Tech. II Semester Regular Examinations May 2018

Power System Analysis

(Electrical & Electronics Engineering)

Max. Marks: 70

3. a)

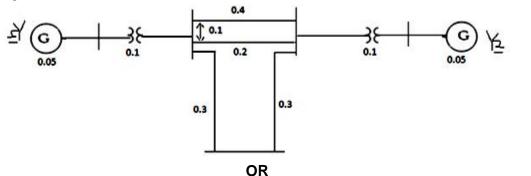
Time: 3 Hours

R-15

Answer all five units by choosing one question from each unit ($5 \times 14 = 70$ Marks)

UNIT–I

1. For the network shown in fig below form the network matrix Y_{BUS} by using singular transformation.



- 2. a) From fundamentals derive the expressions for the step by step algorithm to form Z_{BUS} when a link p q is added to the partial network. 10M
 - b) Define primitive network. Write the performance equations of a primitive network. 4M

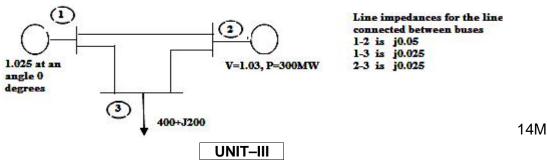
8M

14M

b) Write an algorithm for Newton-Raphson load flow using rectangular coordinates method.

OR

4. The impedances are calculated in 100MVA base. Perform NR load flow for first iteration by using rectangular Coordinates method and consider bus 1 as slack bus for figure below.



- 5. a) Derive the expression for fault current for a LG fault at the terminals of an unloaded alternator. Show the interconnection of sequence networks.
 - b) A 25MVA, 11kV Synchronous Generator has positive, negative and zero sequence reactances of 12%, 12% and 8% respectively. The generator neutral is grounded through a reactance of 5%. A Single line to Ground fault occurs at the generator terminals. Determine fault current and line to line voltages. Assume that the generator is unloaded before fault.

7M

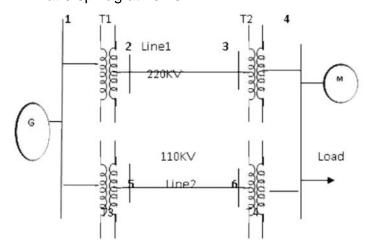
7M

6. a) The one line diagram of a 3-phase power system is shown in figure. Draw the reactance diagram with all impedances including the load impedance marked in per unit. Select a common base of 100MVA

and 22KV on the generator side.

22KV	x=18%
22/220KV	x=10%
220/11KV	x=6%
22/110KV	x=6.4%
110/11KV	x=8%
10.45KV	x=18.5%
	22/220KV 220/11KV 22/110KV 110/11KV

Lines 1 and 2 have reactances of 48.4Ω and 65.43Ω respectively. The three phase load at bus 4 absorbs 57MVA at 0.6pf lag at 10.45KV.



b) Define per unit system and write the advantages of per unit system. 4M

- 7. a) Explain various methods of improving steady state stability. 7M
 - b) Derive the power angle equation, draw the power angle curve and analyze. 7M

OR

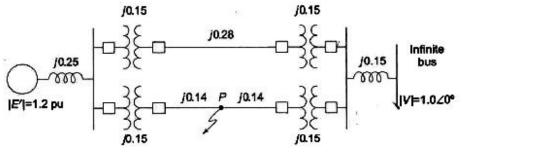
- 8. a) Find the steady state power limit of a system consisting of a generator equivalent reactance 0.5 pu connected to an infinite bus through a series reactance of 1.0 pu. The terminal voltage of the generator is held at 1.2 pu and the voltage of the infinite bus is 1.0 pu.
 - b) Explain synchronizing power coefficient.



9. Explain point by point method of solving Swing equation by deriving necessary equations. 14M

OR

10. Find the critical clearing angle for the system shown in following fig. for a 3-phase fault at the point P. The generator is delivering 1.0pu power under pre fault conditions.



14M

10M

10M

4M

	C ~	ode: 5G263	
		III B.Tech. II Semester Regular Examinations May 2018	
		Power System Operation and Control (Electrical and Electronics Engineering)	
	1	Max. Marks: 70 Answer all five units by choosing one question from each unit (5 x 14 = 70 Marks)	
		******** UNIT–I	
. a	a)	Explain the following terms with reference to power plants:	
		(i) heat input – power output curve (ii) heat rate input (iii) incremental input and (iv) generation cost .	
b))	Obtain the condition for optimum operation of a power system with 'n' plants including the	
		effect of transmission losses.	
	,	OR	
. a	a)	A system consists of two generating plants with fuel costs of: $C_1=0.03P_1^2+15P_1+1.0$	
		$C_2 = 0.04P_2^2 + 21P_2 + 1.4$	
		The system operates on economic dispatch with 120MW of power generation by each plant. The incremental transmission loss of plant-2 is 0.15. Find the penalty factor of plant-1.	
b))	Explain various factors to be considered in allocating generation to different power stations	
		for optimum operation.	
		Explain and write the mathematical formulation for optimal scheduling of Hydro thermal system. Explain any one solution technique of solving equations with the help of an	
		algorithm. OR	
		What is meant by optimal power flow solution? Write the equations describing an optimal	
		power flow problem. How inequality constraints are considered on dependent variables and on control variables.	
		UNIT-III	
. 2	a)	Write the modeling equations of turbine speed governing system. Develop the block diagram of turbine speed governing mechanism with first order equations.	
b))	Construct a Block Diagram for Generator Load Model and find out the transfer function.	
		OR	
	a)	Derive the first order turbine model. Represent the model in block diagram.	
))	Describe various elements that are to be considered in modeling of an excitation system. UNIT-IV	
a	a)	Draw the LFC block diagram of an isolated power system. Write the dynamic response curve of abaptacing for a stop abaptacing load	
r))	of change in frequency for a step change in load. Derive the expression for change in tie line power and draw its block diagram?	
L.)	OR	
a	a)	Write the state space representation of AGC for single area.	
)	How does load frequency control is achieved by considering economic dispatch.	
a	a)	Describe the performance of uncompensated transmission lines.	
)	Describe the constructional features of a synchronous capacitor. Explain its operation and	
		discuss various applications in power system operation.	
	. \	OR	
	a)	Explain the phenomenon of sub synchronous resonance in power system operations and suggest remedies to overcome this problem.	
b)	Explain series and shunt compensation of lines and discuss their effect on the surge impedance loading of the lines. If shunt compensation is 100%, what happens to SIL and	

	F	fall Ticket Number :	
	С	ode: 5G264	
		III B.Tech. II Semester Regular Examinations May 2018	
		Switch Gear and Protection	
		(Electrical and Electronics Engineering) Max. Marks: 70 Answer all five units by choosing one question from each unit (5 x 14 = 70 Marks)	
	-)		714
1.	a) b)	Describe the terms: (i) restriking voltage (ii) recovery voltage (iii) RRRV Outline the construction, principle of operation and application of vacuum circuit breaker.	7M 7M
	0)	Outline the construction, principle of operation and application of vacuum circuit breaker.	7 111
2.	a)	Derive an expression for the restriking voltage in terms of system voltage, inductance and capacitance, across a C.B. contact when a 3-phase fault takes place. Assume the neutral of the	
		system to be solidly grounded.	7M
	b)	Discuss the process of current chopping in circuit breakers. How to avoid it?	7M
3.	a)	What is a protective relay? Discuss the functional characteristics of a protective relay.	7M
	b)	Classify the various types of over current relays and give their applications along with approximate characteristics.	7M
1	a)	OR What is universal torque equation? Using this equation derive the following characteristics:	
4.	a)	(i) impedance relay (ii) reactance relay (iii)mho relay	7M
	b)	Discuss the merits and demerits of static relays over electromechanical relays.	7M
5.	a)	Describe the construction, principle of operation and applications of Buchholz relay.	7M
		For a 10MVA, 132KV/6.6KV power transformer with delta-star connections, obtain the number of	
		turns each current transformer should have, for the differential scheme to circulate a current of 5A	
		in the pilot wires. OR	7M
6.	a)	Discuss about restricted earth fault protection for alternators.	7M
-	b)	Describe the rotor protection against earth fault.	7M
	-	UNIT-IV	
7.	a)	List various schemes of protection of feeders and compare their performance.	7M
	b)	Discuss the schemes of protection for a parallel feeder fed from (i) one end (ii) both ends. OR	7M
8.	a)	What is meant by 3-zone protection? Draw the schematic diagrams of contact circuits and principle of operation for (i) short length lines (ii) medium length lines.	7M
	b)	Discuss the protection scheme for bus bars.	7M
		UNIT–V	
9.	a)	Outline the advantages of grounding of neutral of the system.	7M
	b)	Discuss the phenomenon of 'Arcing grounds' and suggest the method to minimize it. OR	7M
10.		What are BILs? Discuss their significance in power system studies.	7M
	b)	Describe the construction, principle of operation and application of Valve type lightning arrestor.	7M