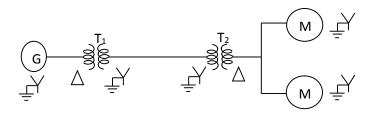
Г														1					
	На	II Ticket Number :												]				R-15	5
	Cod	de: 5G262			6				-					$\sim$					<b>,</b>
		III B.Tech. II Se			•	•									TO	be	r 20	)20	
	Microprocessors and Microcontrollers																		
	( Electrical and Electronics Engineering ) Max. Marks: 70 Time: 3 Hour										Hours								
		Answer all five uni	ts by	chc	osing	g on			n fro	m ec	ach	uni	it (	5 x	:14	. = 7	70 N	1arks	)
								*****											
1.		Explain the functio	n of	tha f	سمالد		NIT-		- with	o cui	tabl		~~	mnl					
1.		DAA, DAS, AAM,				Ũ			5 WIL	i Sui	lau	6 67	۸ai	при	63.				
				D, A		, ––、	), INL 0												
2.		With a neat sketch	expla	ain in	deta	il the	-		rchite	ecture	e of	808	86 I	micı	ropi	roc	esso	or.	
						U	NIT-												
3.	a)	Explain the archited	cture	of 82	253 P	rogra	amma	able I	nterv	al Tir	mer								
	b)	Differentiate betwee	en m	emor	y ma	pped			'O ma	appe	d l/	Ό.							
							0		_		_								
4.	a)	Draw and discuss																	
	b)	What is the advanta	age c	of DM	A Co	ontrol	led d	ata tr	ansfe	er ove	er ir	nterr	up	t dri	iver	n da	ata t	ransf	fer?
						U	NIT-I												
5.	a)	What is basic struct	ture o	of SR	AM a	and D	RAM	1.?											
	b)	What is the need of	DM/	A and	d exp	lain i	t.												
							0	R											
6.	a)	Differentiate SRAM	and	DRA	М.														
	b)	Explain the archited	cture	of 82	257.														
							NIT-I	V											
7.	a)	Distinguish betwee	n svr	chro	nous				nous	data	forr	nats	;?						
	ري b)	With the help of flow	•				-							be s	sent	t fro	om a	a port	t line
		using software rout			•		,											I	
							0	R											
8.		Explain the block d	iagra	m an	d the	func	tions	of ea	ach b	lock	of t	he 8	25	1 U	JSA	RT			
9.		Evoloin the Address	aina	mode	o of		NIT-Y		trollo	r									
э.	a) b)	Explain the Address Explain the functio	•								ollor	2							
	5)			Jaur	, poil	nor I	000 II		5,000	onne	100	•							
0.	a)	Write and explain the	ne dif	ferer	nces	of be	twee	n mic	ropro	ocess	sors	and	n b	nicro	000	ntro	oller	S	
	,								-										

b) List the features of ARM microcontrollers.

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	Hall	Ticket Number :												[]
L	Cod	e: 5G466											-	R-15
	III B.Tech. II Semester Supplementary Examinations October 2020													
		Object Oriented Programming Concepts ( Electrical and Electronics Engineering )												
	Ma	x. Marks: 70	( Ele	ectri	calo	and	Elec	tron	ics E	ngin	eeri	ing )		Time: 3 Hours
	-	Answer all five uni	ts by	cho	osing	g one	e qu	estio	n froi	n ec	nch i	unit (	5 x 14	
			,		,		****	****						,
					_		JNIT							
1.	,	List and explain th				•					•			
	b)	What is a referenc	e var	able	?EX	plain		•	e of re	eterei	nce v	variai	ole.	
~	-)	Write abort pate of	o doo	truct	or E	voloir	<b>0</b>		abla /					
2.	,	,												
	b)	Denne constructor	. <b>Ξ</b> χμ	nain (	uner	entty	/pes		nstru	ciors	with	exa	npie.	
							JNIT-							
3.	a)	What is a friend fu	nctio	n? Ex	xolair				merit	ts of t	frien	d fun	ction.	
0.	с, b)	Explain the differen			•									
	~)	r			-		0							
4.	a)	What is operator o	verlo	ading	g? W	rite a	n prog	gram	to ov	erloa	d the	e + o	perator	
	b)	When do you use	virtua	al bas	se cla	ass?	Expla	ain wi	th su	itable	e exa	ample	)	
						U	INIT-	-111						
5.	a)	What are the operation	ators	avai	lable	in ja	va? E	Expla	in the	em in	deta	ail.		
	b)	List five major diffe	erenc	e bet	tweer	n JA∖	/A ar	nd C+	+.					
							0							
6.	a)	List the various da								•				
	b)	Explain the following	ng st	ring h	nandl	ings	with	suital	ole ex	kamp	le.			
7	-)	Write about the fre			and		NIT-			\^/rite		roarc	m to in	nort classes
7.	a)	Write about the fre from package.	quer	itty u	seu J	lava	ΑΓΙμ	acka	iges.	vvrite	εap	rogra		ipon classes
	b)	Write the steps inv	volve	d in a	dding	g a cl	lass t	oap	acka	ge				
	,						0	-		•				
8.	a)	Discuss the proces	ss of	throv	ving	own e	exce	otions	s in Ja	ava.				
	b)	Explain various ke	ywor	ds in	exce	eptior	n han	dling	with	Java				
			_				JNIT-							
9.	,	Discuss the Life C	-				-					-		
	b)	Explain the implen	nenta	ation	ot yie	eld ()			) met	thods	s thro	bugh	an exa	mple program.
10.	a)	How are paramete	ers da	assec	l to a	n apr	<b>O</b> blet?		ain w	ith ar	ı exa	ample	e proar:	am.
	b)	How do applets dif	-					-			27.0		1.290	
	~,	11	-	-			-	**						

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	На	Il Ticket Number : R-15	
	Coc	de: 5G261	
		III B.Tech. II Semester Supplementary Examinations October 2020	
		Power System Analysis	
	Mc	( Electrical and Electronics Engineering ) ax. Marks: 70 Time: 3 Hours	
	IVIC	Answer all five units by choosing one question from each unit ( 5 x 14 = 70 Marks )	
		*****	
	,	UNIT-I	
1.	a)	Form $Z_{BUS}$ for the following power system .Take bus-1 as reference bus.	
		(1) j0.2 (2)	
		j0.3 j0.4	
		$\overline{(3)}$	4014
	L)	Derive the primitive element and write expression of primitive network in impendence form	10M
	b)	Derive the primitive element and write expression of primitive network in impendence form and admittance form	4M
		OR	
2.	a)	Write the mathematical modeling of different power system elements	6M
	b)	Derive the expression of Y <sub>bus</sub> using singular Transformation method	8M
		UNIT–II	
3.	a)	Explain the classification of buses	6M
	b)	Write an algorithm for Newton-Raphson load flow using rectangular coordinates method. <b>OR</b>	8M
4.	2)	Write an algorithm for the load flow solution using NR method polar co-ordinates.	7M
4.	a) b)	Compare the Gauss-Seidel method, Newton Rapshon method and Fast decoupled method	7M
	0)		
5.	a)	What are the advantages of Per unit system of representation? Define and explain the	
	,	impedance and reactance diagrams with an example.	7M
	b)	Derive the expression for the fault current of an unloaded alternator when it is subjected to a	
		Double -line to Ground fault	7M
~		<b>OR</b> $A = 00 M M A = 11 k M B B B B B B B B B B B B B B B B B B$	
6.		A 90 MVA, 11kV,3-Phase generator has a reactance of 25%. The generator supplies two motors through transformers and transmission line as shown in figure. The transformer $T_1$ is	
		a 3-Phase transformer, 100MVA, 10/132 kV,6% reactance. The transformer T <sub>2</sub> is composed	
		of 3 single phase units each rated at 30 MVA, 66/10KV with 5% reactance. The connections	
		of $T_1$ and $T_2$ are as shown. The motors are rated 50MVA and 40 MVA both 10 KV and 20% reactance .Taking the generator rating as base ,draw reactance diagram and indicate the	
		reactance . Taking the generator rating as base, draw reactance diagram and indicate the	



3-Phase fault is occurred at the terminals of the generator.

reactances in P.U. The reactance of the line is 100 . Also find the fault current when a

14M

		UNIT–IV					
7.	a)	Derive the expression for the steady state stability limit.					
	b)	Prove that maximum power transfer can be achieved when X= 3 R	7M				
		OR					
8.	a)	Explain the synchronizing power coefficient and analyze the system stability using power angle curve	7M				
	b)	Explain the methods to improve the steady state stability limit					
			7M				
		UNIT-V					
9.	a)	Explain the methods to improve transient stability	7M				
	b)	A salient pole synchronous generator is connected to an infinite bus via a line. Derive an expression for electrical power output of the generator and draw p- Curve	7M				
		OR					
10.	a)	What are the assumptions made in deriving the swing equation	7M				
	b)	Explain point by point method of solving swing equation	7M				
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	Hall	Ticket Number :	7
C	Cod	e: 5G263	
		III B.Tech. II Semester Supplementary Examinations October 2020	
		Power System Operation and Control	
	Ma	( Electrical and Electronics Engineering ) x. Marks: 70 Time: 3 Hours	
	-	Answer all five units by choosing one question from each unit ( 5 x 14 = 70 Marks )	
		******	
		UNIT-I	
1.		Explain the following terms with reference to the power plants: Heat input, Power output curve, Heat rate input, Incremental input, Generation cost and Production cost.	14M
		OR	14111
2.	a)	Explain the need of economic load dispatch for a given power system	5M
	b)	Derive the transmission loss formula for a system consisting of n-generating plants	
		supplying several loads inter connected through a transmission networks. State any	
		assumptions are made.	9M
		UNIT–II	
3.	a)	Explain in detail about short-term Hydro thermal scheduling problem	8M
	b)	What are the advantages of operation of hydro thermal combinations?	6M
		OR Develop the hydroclostric resume plant readel with response with a line response.	4 4 5 4
4.		Develop the hydroelectric power plant model with necessary block diagram.	14M
5.	a)	Derive the small signal transfer function with Block diagram of speed governing system.	7M
0.	b)	Draw the block diagram of IEEE type-1 excitation model and explain.	7M
	~)	OR	
6.	a)	Derive the generator load model and represent it by a block diagram.	7M
	b)	Explain the block diagram representation of an isolated power system with diagram.	7M
		UNIT–IV	
7.	a)	Explain the necessity of maintaining a constant frequency in power system operation	7M
	b)	Draw the block diagram of single area LFC system with integral control and prove that	
		the steady state change in frequency is zero.	7M
•		OR	
8.		Draw the block diagram for two area load frequency control with integral controller blocks and explain each block.	14M
		UNIT-V	
9.	a)	Explain about shunt compensation.	7M
	b)	Explain the effect of uncompensated line under no load and load condition	7M
		OR	
10.	a)	Explain briefly the specifications and objectives of load compensation.	7M
	b)	Compare the different types of compensating equipment for transmission systems.	7M
		***	