Hall Ticket Number :														r		
Code: 5GA61									I					R·	-15	
	III B.Tech. II Semester Supplementary Examinations Nov/Dec 2018															
	Management Science															
	(Electrical and Electronics Engineering)															
	Max. Marks: 70 Time: 3 Hours													Irs		
Answer all five units by choosing one question from each unit (5 x 14 = 70 Marks)																
	UNIT–I															
1	 Define management. Explain nature and significance of Management science. OR 															
2	2.	Explain the princ	ciples	of ma	inage	emer	nt as o	outline	ed by H	ler	nry Fa	ayol.				
	UNIT–II															
3	3. a)	What are the dif	ferent	level	s of c	hanr	nels c	of distr	ibutior	า?						
	b)	Explain its impo	rtance	in m	arket	ing.	_	_								
			Ξ.				0									
2	I. a)	What is meant I	•					ant La	yout?							
	b)	Explain differen	t meti	nods	of pr											
	-		~ ** .:	م مد ما	:40.00				~~ \ \ / h		oro 1			-f		
:	5.	How the term re	cruitm	ient a	mere	ent iro	om se O		n? vvr	a	are u	ne so	urces	Directuit	ment?	
6	б.	Define Human			e Ma	anag			kplain	ał	bout	the	signifi	cance o	of Hur	nan
		Resource Mana	geme	ent.				n /								
_	7.	Define Financia	l Mon	2000	oont?				e ite e		no?					
'	•		i wan	ayen	ICIII :	Ы	olieniy (O l		5 115 5	500	hei					
8	3.	With the help	of t	he f	ollow	/ing	data	. dra	w the	e	netw	ork.	(a) D	raw the	e netv	vork
	 8. With the help of the following data, draw the network. (a) Draw the network (b) Find project duration for the following project and (c) Identify the critical path 															
		Activity	1-2	1-3	1-	-4	2-4	2-5	3-4		3-7	4-6	6 4-7	<i>5</i> -6	5-7	
		Time (months)	4	6	1	2	7	11	7		8	8	13	4	4	
							UNIT	–V								
ę	9.	What is Enterpr	ise R	esoui	ce P	lann	•		re it is	ap	oplica	ble?	Expla	in in det	ail?	
							0	К								

10. Explain the nature and objectives of Ethics.

Hall T	icke	t Number :	
Code:		R-15	
		ech. II Semester Supplementary Examinations Nov/Dec 2018	
	2	Microprocessors and Microcontrollers	
		(Electrical & Electronics Engineering)	
Max. I An	-	ks: 70	Jrs
		UNIT–I	
1.	a)	Distinguish minimum mode and maximum mode of operation	8N
	b)	Explain the instruction format of 8086µP with an example	6M
		OR	
2.	a)	Advantages of procedures used in 8086 μ P? Illustrate the syntax of it.	8M
	b)	List out the assembler directives used in 8086µP.	6M
2		UNIT-II	71/
3.	a) b)	Describe about memory mapped I/O Interfacing.	7N 7N
	b)	Describe the stepper motor interfacing with 8086 μP. OR	7N
4.	a)	Describe the A/D converters interfacing with 8086 µP.	7N
4.	a) b)	Write an assembly language program to interface 8255 in mode-0 with 8086.	7N
	5)	while an assembly language program to interface ozoo in mode o with ocoo.	710
		UNIT-III	
5.	a)	Explain about 4KB of RAM Memory interfacing with 8086.	7N
	b)	Explain about modes of 8257.	7N
		OR	
6.	a)	Illustrate the basic structure of SRAM and DRAM cells	7N
	b)	Need for DMA? Explain the master and slave modes of DMA.	7N
		UNIT–IV	
7.	a)	Describe 8259 PIC architecture.	7N
	b)	Describe TTL to RS232C and RS232C to TTL conversion.	7N
	,	OR	
8.	a)	Describe 8251 USART architecture.	7N
	b)	Describe Vector interrupt table of 8086 µP.	7N
9.	a)	UNIT–V Explain about RAM organization in 8051 µC.	7N
0.	b)	Explain about Timer/Counters of 8051µC.	7N
	~)	OR	
10.	a)	Draw the pin diagram 8051µC.	7N
	b)	Describe the addressing modes 8051µC.	7N
	- /	***	

Hall	Tick	et Number :						
Code: 5G466								
Couc		B.Tech. II Semester Supplementary Examinations Nov/Dec 2018 Object Oriented Programming Concepts (Electrical and Electronics Engineering)						
		arks: 70 Firme: 3 Hou Firme: 4 Hou Firme: 4 Hou Firme: 5 Hou Firme: 6 Hou Firme: 6 Hou Firme: 6 Hou Firme: 6 Hou Firme: 6 Hou Firme: 7	Jrs					
1.	a)	UNIT–I Explain Object oriented programming paradigm. Distinguish between Objects and Classes.	8M					
	b)	Briefly explain the merits and demerits of Object Oriented Programming.	6M					
2.	a)	What are constructors? Explain constructor overloading with an example program.	7M					
	b)	Describe the benefits offered by OOP. What is dynamic binding? Explain how it works with an example.	7M					
3.	a)	UNIT–II What is inheritance? Write a C++ program explaining multiple inheritances.	7M					
0.	b)	What is pure virtual function? Write a program to demonstrate polymorphism of functions.	7M					
		OR						
4.	a)	What are friend functions? Write a program to demonstrate the concept of friend functions.	8M					
	b)	Give a program to explain the concept of Virtual base class.	6M					
5.	a)	Explain about decision making statements in Java.	7M					
	b)	List the eight basic data types in Java with examples.	7M					
c	c)	OR Mantian the basis parts of Mathada Declaration in Jova Discuss the concent						
6.	a)	Mention the basic parts of Methods Declaration in Java. Discuss the concept of method overloading in Java.	7M					
	b)	What is a string buffer class? Write a program to arrange the strings in alphabetical order.	7M					
7.	a)	What is an interface? Distinguish between an interface and a class.	8M					
	b)	Write a program to explain the process of accessing interface variables.	6M					
		OR						
8.	a)	What are exceptions in Java? Write about the common exceptions that occur in Java.	7M					
	b)	Explain exception handling mechanism in Java.	7M					
9.	a)	Discuss the Life Cycle of a Thread using a state transition diagram.	8M					
	b)	Explain the implementation of yield () and stop () methods through an example program.	6M					
		OR						
10.	a) b)	How are parameters passed to an applet? Explain with an example program. How do applets differ from application programs?	7M 7M					

Hall Ti	cket Number : P 15]
Code:	5G261 R-15	
	B.Tech. II Semester Supplementary Examinations Nov/Dec 2018	
	Power System Analysis	
	(Electrical & Electronics Engineering)	
	Marks: 70 Time: 3 Hours	
An	swer all five units by choosing one question from each unit (5 x 14 = 70 Marks)	
	UNIT-I	
1. a)	Write the mathematical modeling of different power system elements.	7
b)	Starting from the first principles show that a diagonal element of Y_{BUS} equal to	
	the sum of admittances connected to that bus and an off diagonal element equal to	
	the negative of the admittance directly connected between the buses.	71
	OR	
2.	For the network shown in fig below, form Z_{BUS} using step by step algorithm.	
	j0. <u>3</u>	
	j0.2 j0.15	
	j1.2 j1.5	
		14
	UNIT-II	
3. a)	Write an algorithm for Gauss-Seidal load flow method including PV buses.	8
b)	What are the different types of buses as categorized for load flow studies, explain	
	each one of them in detail.	6
	OR	
4.	For the power system network, the generators are connected at all four buses, while loads are at buses 2, 3 and 4. The values of real and reactive powers are	

while loads are at buses 2, 3 and 4. The values of real and reactive powers are listed in table 1. All buses other than slack bus are of P-Q type. Line data are given in table 2. Assuming a flat voltage start, determine the voltage magnitudes and the phase angles at the three buses using G- S(Gauss-Seidel) method for first iteration.

Table 1. Bus data											
Bus P		i	Qi	Vt	Type of bus						
1		-		1.05 0	Slack						
2	-0	.45	-0.15		PQ						
3	-0	.51	-0.25		PQ						
4 -0		.6	-0.3		PQ						
Table 2. Line data											
Line No. Bus Co			de(p-q)	de(p-q) Line Impedance							
1		1-2		0.08+j0.2							
2		1-4		0.05+j0.1							
3		2-3		0.04+j0.12							
4		3-4		0.04+j0.14	,						

14M

UNIT–III

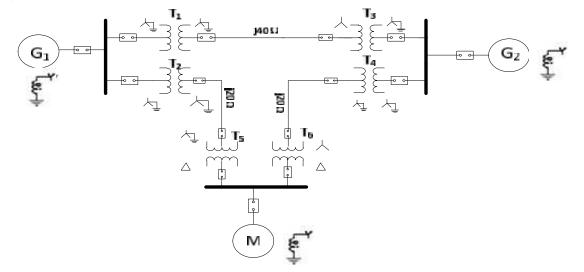
5. a) Draw the reactance diagram for the power system shown in below fig. The ratings of generator, motor and transformers are given below. Neglect resistance and use a base of 50MVA, 138KV in the 40 line.

Generator G1: 20 MVA,18KV,X"=20%;

Generator G₂: 20 MVA,18KV,X"=20%;

Synchronous Motor: 30MVA, 13.8KV, X"=20%;

- 3-phase Transformer: 20MVA, 138KV/20KV, X=10%;
- 3-phase Transformer: 20MVA, 138KV/20KV, X=10%;



b) Three phase voltages across a certain unbalanced 3Φ load are given as $E_R = 176$ -j132; $E_Y = -128$ -j96 and $E_B = -160$ +j100. Find Positive, Negative and Zero sequence components of voltages.

OR

- 6. a) Explain interconnection of sequence networks for a LL-G fault in power system network with necessary equations.
 - b) A 25MVA, 11kV Synchronous Generator has positive, negative and zero sequence reactances of 12%, 12% and 8% respectively. The generator neutral is grounded through a reactance of 5%. A Single line to Ground fault occurs at the generator terminals. Determine fault current and line to line voltages. Assume that the generator is unloaded before fault.

- 7. a) Prove that maximum power transfer can be achieved when X= 3 R. 7M
 - b) Explain various methods to improve Steady State Stability.

OR

- 8. a) Derive the expression for the steady state stability limit.
 - Explain the synchronizing power coefficient and analyze the system stability using power angle curve.
 7M

9. a) Explain various methods of improving transient stability.

b) Derive the Swing equation of a Synchronous machine.

OR

- 10. a) With the help of Equal area criterion for one machine connected to Infinite bus, derive the expressions for critical clearing angle and critical clearing time.
 - b) A 50HZ generator is delivering 50% of the power that is capable of delivering through a transmission line to an infinite bus. A fault occurs that increases the reactance between generator and infinite bus 500% of the value before fault. When the fault is isolated the maximum power that can be delivered is 75% of the original maximum value. Determine the critical clearing angle for the condition delivered.

6M

8M

6M

7M

7M

7M

7M

7M

7M

8M

		Hall Ticket Number : R-15	
		Code: 5G263 III B.Tech. II Semester Supplementary Examinations Nov/Dec 2018	
		Power System Operation and Control	
		(Electrical and Electronics Engineering)	
		Max. Marks: 70 Time: 3 Hours	
		Answer all five units by choosing one question from each unit (5 x 14 = 70 Marks)	
		UNIT-I	
	a)	Explain the significance of equality and inequality constraints in the economic	
	,	allocation of generation among different plants in a system	
	b)	A system consists of three generating plants with fuel costs of:	
		$C_1 = 0.04 P_1^2 + 20 P_1 + 230 \text{ Rs./h}$	
		$C_2=0.06P_2^2+18P_2+200 \text{ Rs./h}$	
		$C_3=0.15P_3^2+15P_3+180$ Rs./h	
		Determine the optimum sharing of a total load of 180MW for which each plant would take up for minimum input cost of received power in Rs/MWh.	
		OR	
	a)	What are Loss coefficients? Derive the expressions for Loss coefficients of a two generator	
		system.	
	b)	Draw the flow chart for obtaining optimal scheduling of generating units by neglecting the	
		transmission losses.	
	a)	Explain problem formation and solution procedure of optimal scheduling for hydro thermal plants.	
•	a) b)	A load is fed by two plants, one is thermal and other is a hydro plant. The load is located near	
	5)	the thermal plant. The characteristics of the plants are	
		$F_T = 0.04P_T^2 + 25PT + 20 Rs./hr; W_H = 0.0012P_H^2 + 7.5P_H m3/Sec;$	
		$x_{H}=2.5 \times 10^{-3} Rs./m^{3}$. Determine the power generation of both plants and load connected, when λ	
		= 20Rs./ MWh.	
		OR Obtain the modeling of hydro turbing and draw its block diagram	
•	a) b)	Obtain the modeling of hydro turbine and draw its block diagram. With the help of a flow chart, explain the dynamic programming method in unit commitment.	
	0)	UNIT-III	
	a)	Explain speed governing mechanism. Develop its block diagram.	
	b)	Develop the block diagram of Generator and load.	
		OR	
•	a)	Why is it necessary to maintain constant frequency and voltage profiles in a power system	
	L)	network? Explain.	
	b)	Draw and explain the Block diagram of IEEE type-1 excitation system. UNIT-IV	
	a)	Draw the block diagram of single area Load frequency control system. Explain the terms in it.	
•	с) b)	Two generators of rating 125 and 250MW are operated with droop characteristics of 4% and 5%	
	- /	respectively from no load to full load. Find the load sharing by each generator if a load of 300MW	
		is connected across the parallel combination of those generators.	
	-	OR Chevy that steady state frequency deviation in a single area LEC is reduced to zero if the DL	
•	a)	Show that steady state frequency deviation in a single area LFC is reduced to zero if the PI controller is reduced.	
	b)	Discuss the importance of combined load frequency control and economic dispatch control with	
	2)	a neat block diagram.	
		UNIT-V	
	a)	What do mean by compensation of a line? Discuss briefly different methods of compensation.	
	b)	Explain what you mean by loadability of overhead lines and discuss loadability characteristic of	
		these lines.	
	<i>а)</i>	OR What is sub synchronous resonance condition? How is it handled in electrical network?	
•	a) b)	A 35 kW induction motor has power factor 0.85 and efficiency 0.9 at full load, power factor 0.6	
	U)	and efficiency 0.7 at half-load. At no-load, the current is 25% of the full-load current and power	
		factor 0.1. Capacitors are supplied to make the line power factor 0.8 at half-load. With these	
		capacitors in circuit, find the line power factor at (i) full load, and (ii) no-load.	

ŀ	lall ⁻	Ticket Number :											
С	ode	: 5G264 R-15											
		III B.Tech. II Semester Supplementary Examinations Nov/Dec 2018											
		Switch Gear and Protection											
		(Electrical and Electronics Engineering)											
		ax. Marks: 70 Inswer all five units by choosing one question from each unit (5 x 14 = 70 Marks)											
	7.11	**************************************											
		UNIT–I											
1.	a)	Describe the construction, principle of operation and application of SF_6 circuit breaker.	7M										
	b) In a short circuit test on a 132KV 3-phase system, the breaker gave the following results: p.f. of the fault 0.4, recovery voltage 0.95 of full line value; the breaking current is symmetrical and the symmetrical and the fault of the breaking current is symmetrical and the fault of the breaking current is symmetrical and the fault of the breaking current is symmetrical and the fault of the breaking current is symmetrical and the fault of the breaking current is symmetrical and the fault of the breaking current is symmetrical and the fault of the breaking current is symmetrical and the fault of the breaking current is symmetrical and the fault of the breaking current is symmetrical and the breaking current is sym												
		restriking transient had a natural frequency of 16KHZ. Determine the rate of rise of restriking voltage. Assume that fault is grounded.	7M										
		OR											
2.	a)	Compare the performance and characteristics of minimum oil breakers and air blast C.B.	7M										
	b)	Describe the terms: (i) symmetrical breaking current (ii) Asymmetrical breaking current (iii) Making											
		current.	7M										
	,												
3.	a)	What is meant by 'dead zone' when referred to directional relay and discuss how it is taken care of.	7M										
	b)	Describe the construction, principle of operation and application of induction disc type of relay. OR	7M										
4.	a)	Explain what is meant by primary protection and backup protection.	7M										
	b)	Draw the connection diagram of micro processor based reactance relay and describe its operation.	7M										
		UNIT-III											
5.	a)	Discuss the abnormal conditions in a large alternator against which protection is necessary.	7M										
	b)	Outline inter- turn fault protection in alternators.	7M										
0	-)	OR											
6.		Discuss the basic principle of operation of a percentage differential relay for (i) internal fault (ii) through fault.	7M										
	b)	A 3-phase transformer rated for 33KV/6.6KV is connected star/delta and the protecting current transformer on the low voltage side have a ratio of 400/5. Determine the ratio of current transformer											
		on the HV side.	7M										
		UNIT-IV											
7.	a)	Explain how the selection of current and time settings is done in a time-current graded system.	7M										
	b)	Discuss the protection of radial and ring main feeders.	7M										
		OR											
8.		Discuss the carrier system of protection.	14M										
		UNIT-V											
9.	a)	Outline the advantages of (i) grounding the neutral of the system (ii) keeping the neutral isolated.	7M										
	b)	A 50HZ overhead line has line to earth capacitance of 1μ F. It is decided to use an earth fault											
		neutralizer. Determine the reactance to neutralize the capacitance of (i) 100%length of line (ii) 90%length of line (ii) 50%length of line.	7M										
	OR												
10.	a)	Describe the construction, principle of operation and application of Zinc oxide lightning arrestor.	7M										
	b)	What are the causes of over voltages? Mention the methods of protection against lightning over											
		voltages.	7M										