

Hall Ticket Number :

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**R11**

**Code: 1G48C**

*IV B.Tech. II Semester Supplementary Examinations Nov/Dec 2016*

**Database Management Systems**

(Electronics and Communication Engineering)

Max. Marks: 70

Time: 3 Hours

Answer any **five** questions

All Questions carry equal marks (14 Marks each)

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|------|---|-----|
| 1 a) | What is E-R diagram?  | 2M  |
| b)   | Design E-R diagram for college library management system  | 12M |
| 2 a) | Explain about Logical Database Design?  | 8M  |
| b)   | What is mean by Tuple Relation Calculus & Doman Relation Calculus?  | 6M  |
| 3    | What is mean by Trigger? What is the impact of Triggers on RBMS?  | 14M |
| 4    | Explain about De-normalization with suitable examples?  | 14M |
| 5    | Explain ACID Properties of transactions?  | 14M |
| 6    | Explain about deadlock and 2-phase locking to ensure serialziability in concurrency control with locking? | 14M |
| 7    | What is mean by index? Explain Different types of Indexes?  | 14M |
| 8 a) | How we are Implementing Stable Storage Structures in DBMS?  | 7M  |
| b)   | Explain why local depth and global depth are needed in Extendible Hashing                                 | 7M  |

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Hall Ticket Number :										
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<b>R-11</b>
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**Code: 1G383**

*IV B.Tech. II Semester Supplementary Examinations Nov 2016*

**DSP Processors and Architectures**

(Electronics and Communication Engineering)

Max. Marks: 70

Time: 3 Hours

Answer any **five** questions

All Questions carry equal marks (14 Marks each )

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1. a) With example prove that pipelining improve the performance. 7M  
b) How Multiplier & Multiplier accumulator help in execution of DSP equations. 7M
2. a) With suitable examples explain number formats used in DSP. 7M  
b) Explain different errors in A/D and D/A conversion. 7M
3. a) Write short notes on different computational blocks in DSP. 10M  
b) Discuss addressing generation unit with suitable diagram. 4M
4. a) Discuss different On-Chip Peripherals TMS320C54XX Processors. 7M  
b) Discuss different addressing modes of P-DSP. 7M
5. a) Implement the Decimation Filters using basic DSP algorithms 7M  
b) Implement the IIR Filters using the relevant DSP algorithm 7M
6. a) Explain 8Point FFT implementation on the TMS320C54XX 10M  
b) Discuss Bit-Reversed Index Generation. 4M
7. a) Explain the CODEC-DSP interface example. 10M  
b) Discuss interrupt I/O method. 4M
8. a) Design flow for an FPGA based system design 7M  
b) Discuss different application nodes on DSP systems 7M

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